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Approved for use through 10/31/2002. OMB 0651-0032

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**UTILITY  
PATENT APPLICATION  
TRANSMITTAL**

(Only for new nonprovisional applications under 37 CFR 1.53(b))

Attorney Docket No.

I-2-148.1US

First Inventor

Nolan et al.

Title

COMMUNICATION TRANSMISSION IMPAIRMENT  
EMULATOR

Express Mail Label No.

EL649759892

**APPLICATION ELEMENTS**

See MPEP chapter 600 concerning utility patent application contents.

1. ☒ Fee Transmittal Form (e.g., PTO/SB/17)  
(Submit an original and a duplicate for fee processing)
2. ☐ Applicant claims small entity status.  
See 37 CFR 1.27.
3. ☒ Specification [Total Pages  ]  
(preferred arrangement set forth below)
- Descriptive title of the invention
  - Cross Reference to Related Applications
  - Statement Regarding Fed sponsored R & D
  - Reference to sequence listing, a table, or a computer program listing appendix
  - Background of the Invention
  - Brief Summary of the Invention
  - Brief Description of the Drawings (if filed)
  - Detailed Description
  - Claim(s)
  - Abstract of the Disclosure
4. ☒ Drawing(s) (35 U.S.C. 113) [Total Sheets  ]
5. Oath or Declaration [Total Pages  ]
- a. ☐ Newly executed (original or copy)
- b. ☐ Copy from a prior application (37 CFR 1.63 (d))  
(for continuation/divisional with Box 17 completed)
- i. ☐ **DELETION OF INVENTOR(S)**  
Signed statement attached deleting inventor(s)  
named in the prior application, see 37 CFR  
1.63(d)(2) and 1.33(b).
6. ☐ Application Data Sheet. See 37 CFR 1.76

**ADDRESS TO:**Commissioner for Patents  
Box Patent Application  
Washington, DC 20231**ACCOMPANYING APPLICATION PARTS**

9. ☐ Assignment Papers (cover sheet & document(s))
10. ☐ 37 CFR 3.73(b) Statement ☐ Power of Attorney  
(when there is an assignee)
11. ☐ English Translation Document (if applicable)
12. ☐ Information Disclosure Statement (IDS)/PTO-1449 ☐ Copies of IDS Citations
13. ☐ Preliminary Amendment
14. ☒ Return Receipt Postcard (MPEP 503)  
(Should be specifically itemized)
15. ☐ Certified Copy of Priority Document(s)  
(if foreign priority is claimed)
16. ☒ Other: \*Declaration is unsigned; Communication Under Rule 153(b).

17. If a CONTINUING APPLICATION, check appropriate box, and supply the requisite information below and in a preliminary amendment, or in an Application Data Sheet under 37 CFR 1.76:

☐ Continuation ☐ Divisional ☐ Continuation-in-part (CIP)

of prior application No. \_\_\_\_\_ / \_\_\_\_\_

Prior application information:

Examiner \_\_\_\_\_

Group / Art Unit, \_\_\_\_\_

For CONTINUATION OR DIVISIONAL APPS only: The entire disclosure of the prior application, from which an oath or declaration is supplied under Box 5b, is considered a part of the disclosure of the accompanying continuation or divisional application and is hereby incorporated by reference. The incorporation can only be relied upon when a portion has been inadvertently omitted from the submitted application parts.

**18. CORRESPONDENCE ADDRESS**☒ Customer Number or Bar Code Label

24374

or ☐ Correspondence address below

(Insert Customer No. or Attach bar code label here)

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DEPT ICC

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City

State

Zip Code

Country

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Name (Print/Type)

Gerald B. Halt, Jr., Esquire

Registration No. (Attorney/Agent)

37,633

Signature

Date 11/15/00

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Volpe and Koenig, P.C. Revision of PTO/SB/17 (08-00)

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**FEE TRANSMITTAL  
for FY 2000**

Patent fees are subject to annual revision.

**TOTAL AMOUNT OF PAYMENT**

(\$ 746

**Complete if Known**

Application Number	Not Yet Known
Filing Date	Not Yet Known
First Named Inventor	Nolan et al.
Examiner Name	Not Yet Known
Group Art Unit	Not Yet Known
Attorney Docket No.	I-2-148.1US

**METHOD OF PAYMENT (check one)**

- 1.
- ☒
- The Commissioner is hereby authorized to charge indicated fees and credit any overpayments to:

Deposit Account Number	09-0435
Deposit Account Name	InterDigital Comm. Corp.

- ☒ Charge Any Additional Fee Required Under 37 CFR 1.16 and 1.17
- ☐ Applicant claims small entity status. See 37 CFR 1.27

- 2.
- ☐
- Payment Enclosed:

☐ Check ☐ Credit card ☐ Money Order ☐ Other

**FEE CALCULATION****1. BASIC FILING FEE**

Large Entity Fee Code (\$)	Small Entity Fee Code (\$)	Fee Description	Fee Paid
101 710	201 355	Utility filing fee	710
106 320	206 160	Design filing fee	
107 490	207 245	Plant filing fee	
108 710	208 355	Reissue filing fee	
114 150	214 75	Provisional filing fee	

**SUBTOTAL (1) (\$ 710)****2. EXTRA CLAIM FEES**

Total Claims	Extra Claims	Fee from below	Fee Paid
22	20** = 2	18	36
3	3** = 0	80	0

\*\*or number previously paid, if greater, For Reissues, see below

Large Entity Fee Code (\$)	Small Entity Fee Code (\$)	Fee Description
103 18	203 9	Claims in excess of 20
102 80	202 40	Independent claims in excess of 3
104 270	204 135	Multiple dependent claim, if not paid
109 80	209 40	** Reissue independent claims over original patent
110 18	210 9	** Reissue claims in excess of 20 and over original patent

**SUBTOTAL (2) (\$ 36)****FEE CALCULATION (continued)****3. ADDITIONAL FEES**

Large Entity Fee Code (\$)	Small Entity Fee Code (\$)	Fee Description	Fee Paid
105 130	205 65	Surcharge - late filing fee or oath	
127 50	227 25	Surcharge - late provisional filing fee or cover sheet	
139 130	139 130	Non-English specification	
147 2,520	147 2,520	For filing a request for <i>ex parte</i> reexamination	
112 920*	112 920*	Requesting publication of SIR prior to Examiner action	
113 1,840*	113 1,840*	Requesting publication of SIR after Examiner action	
115 110	215 55	Extension for reply within first month	
116 390	216 195	Extension for reply within second month	
117 890	217 445	Extension for reply within third month	
118 1,390	218 695	Extension for reply within fourth month	
128 1,890	228 945	Extension for reply within fifth month	
119 310	219 155	Notice of Appeal	
120 310	220 155	Filing a brief in support of an appeal	
121 270	221 135	Request for oral hearing	
138 1,510	138 1,510	Petition to institute a public use proceeding	
140 110	240 55	Petition to revive - unavoidable	
141 1,240	241 620	Petition to revive - unintentional	
142 1,240	242 620	Utility issue fee (or reissue)	
143 440	243 220	Design issue fee	
144 600	244 300	Plant issue fee	
122 130	122 130	Petitions to the Commissioner	
123 50	123 50	Petitions related to provisional applications	
126 240	126 240	Submission of Information Disclosure Stmt	
581 40	581 40	Recording each patent assignment per property (times number of properties)	
146 710	246 355	Filing a submission after final rejection (37 CFR § 1.129(a))	
149 710	249 355	For each additional invention to be examined (37 CFR § 1.129(b))	
179 710	279 355	Request for Continued Examination (RCE)	
169 900	169 900	Request for expedited examination of a design application	

Other fee (specify) \_\_\_\_\_

\* Reduced by Basic Filing Fee Paid

**SUBTOTAL (3) (\$)****SUBMITTED BY**

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Date 11/15/00

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## COMMUNICATION TRANSMISSION IMPAIRMENT EMULATOR

### BACKGROUND OF THE INVENTION

#### Field of the Invention

The present invention relates generally to analog and digital communication systems. More specifically, the invention relates to a system and method for emulating signal impairments which manifest themselves during signal conversion and transmission.

#### Description of the Related Art

In simple terms, a communication system transmits information from a source to a destination. The information signal is transmitted from the source to the destination over an appropriate medium which may be guided or unguided, comprising copper, optical fiber or air and is commonly referred to as the communication channel. To use the channel for transportation, the information signal must be altered to match the characteristics of the channel which is referred to as modulation. The recovery of the information-bearing signal is called demodulation. The demodulation step converts the transported signal using the logical inverse of the modulation process.

A prior art communication system is shown in **FIG. 1**. The communication system in **FIG. 1** shows a single direction communication link for a data signal from one location (user 1) to another (user 2). The prior art system comprises a transmit modem, an up-converter or transmitter, a communication medium, a down-converter or receiver and a receive modem. The transmit modem accepts a data input and produces a modulated digital or analog baseband output according to the

chosen modulation scheme. The modulated data is input to the transmitter where it is upconverted onto a predefined carrier frequency and output to the communication medium. The receiver and receive modem perform a similar inverse operation.

5 Deployed communication systems rarely are single access. Protocols such as time division multiple access (TDMA), carrier sense multiple access (CSMA), code division multiple access (CDMA) and frequency based protocols such as frequency division multiple access (FDMA) and orthogonal frequency division multiplexing (OFDM) allow access to the same communication channel for more than one user. These techniques can be mixed together creating hybrid varieties of multiple access schemes such as time division duplex (TDD). The access protocol specified by a communication system is typically executed after the data undergoes modulation.

10 Prior art modulation techniques that are in use are frequency modulation (FM), frequency shift keying (FSK), phase shift keying (PSK), binary phase shift keying (BPSK) and differential phase shift keying (DPSK). The most commonly used high-speed methods for data modulation are quadrature amplitude modulation (QAM) and quadrature phase shift keying (QPSK). These techniques vary a predefined carrier frequency amplitude and phase according to an input signal to transmit multiple bits per baud thereby using available bandwidth more efficiently.

20 To extend the possible range of data signal values, quadrature modulation assigns a symbol to represent more than two binary values. The concept of a symbol allows for a greater degree of transmitted information since the bit content of each symbol dictates a unique pulse shape. Symbols, which encode  $x$  bits per symbol may represent a quantized version of an analog sample or digital data. Depending upon the number of symbols used, an equal number of unique pulse or wave shapes

exist. The number of data bits determine the combinations of amplitude and phase that define a constellation pattern.

Quadrature modulation is based on two distinct carrier waveforms that are orthogonal to each other. If two waveforms are transmitted simultaneously and do not interfere with each other, they are orthogonal. Quadrature modulation modulates two different signals into the same bandwidth creating a two-dimensional signal space as shown in **FIG. 2a**. Two waveforms generally used for quadrature modulation are sine and cosine waveforms at the same frequency. The waveforms are defined as

$$s_1(t) = a_1 \cos(2\pi f_c t) \quad (1)$$

and

$$s_2(t) = a_2 \sin(2\pi f_c t) \quad (2)$$

where  $f_c$  is the carrier frequency of the modulated signal and  $a_1$  and  $a_2$  are the amplitudes applied to the cosine and sine signals respectively. By convention, the cosine carrier is called the in-phase, real component  $I$  of the signal and the sine carrier is the quadrature, imaginary component  $Q$  of the signal. Linear combinations of the form  $a_1 \cos(2\pi f_c t) + a_2 \sin(2\pi f_c t)$  (where  $a_1$  and  $a_2$  are real numbers) generated from the two basic waveforms define symbols in the modulation alphabet. The symbols can be represented as complex numbers,  $a_1 + ja_2$ , where  $j$  is defined as  $j = \sqrt{-1}$ .

A QAM symbol consists of at least one sample from both the in-phase  $I$  and quadrature  $Q$  signals. Signal amplitude is indicated by the distance from the origin; phase by the angular distance around the unit circle. After the data is assembled as symbols, the symbols are further processed in accordance with the access protocol chosen for the communication system.

The above processing is typically performed in a modem. Afterwards, a predefined carrier frequency is modulated with the baseband output from the modem, amplified and transmitted in the communication medium. Upconversion is required when the channel frequencies are above baseband frequencies. Transmission through a medium is accomplished by converting the modem output signal amplitude, frequency or phase to an operating region between  $10^4$  to  $10^8$  Hz using radio frequency amplifiers,  $10^8$  to  $10^{11}$  Hz using microwave frequency amplification and  $10^{11}$  to  $10^{16}$  Hz using optical frequency amplification. Reception of the communication transmission is by downconversion.

Modulation schemes that rely upon phase must overcome the inevitable problem of phase synchronization. For proper signaling, the  $I$  and  $Q$  channels should have the same gain when processing both received channels, keeping the  $I$  and  $Q$  channels uncorrelated. Mismatched signal gains or magnitudes between the uncorrelated  $I$  and  $Q$  channels create errors when processing. Phase differences other than 90 degrees between the carrier waveform signals cause spillover between individual channels and similarly result in degraded performance. However, during carrier modulation (upconversion), transmission through the communication channel and carrier demodulation (downconversion), signal impairments occur.

Signal impairments which manifest themselves during the conversion processes are gain and phase variations in the separate  $I$  and  $Q$  channels. This is due in part to the plurality of electronic mixers, filters, A/D converters, *etc.*, employed in the design of up and downconverters. Each component contributes its own variation in specified value due to temperature, manufacturing tolerances and other factors until the variations taken as a whole significantly affect signal integrity.

Amplitude and phase imbalance between the  $I$  and  $Q$  channels result in the constellation distortions shown in **FIGs. 2b** and **2c**, decreasing overall signal-to-noise ratio (SNR).

Of these impairments, amplitude and phase impairments are linear distortions. Other significant linear impairments which manifest themselves in a data signal during carrier frequency modulation and demodulation comprise: carrier frequency offset, caused by local (receiver) oscillator drift; carrier phase noise, impressed on the data signal by active devices in the signal path; communication channel bandwidth aberrations, caused by unintentional filtering; group delay variation and carrier amplitude imbalance.

Non-linear impairments are another adverse byproduct. Non-linear distortions are characterized by changes in output gain or phase which vary in dependence upon the input signal magnitude. The two major signal impairments include: amplitude-to-amplitude (AM-AM) distortion caused by non-linearities in the overall amplifier gain transfer function and amplitude-to-phase distortion (AM-PM conversion) distortion caused by amplitude dependent phase shifts.

In addition to the impairments brought about by up and downconversion, the communication media, whether guided or unguided, is under the influence of obstacles, attenuation and wave reflections. These perturbations affect signal level by many dB and continually change in a mobile communication operating environment. The propagation characteristics vary widely depending upon whether a communication link is fixed or mobile, the condition of the propagation path and the composition of the medium itself.

While designing and prototyping new communication systems, manufacturers routinely and thoroughly test the baseband modulation/demodulation components and the up/downconversions to and from the transmission channel operating frequencies. To validate a modem hardware design,

prior art test techniques comprise signal generators,  $E_b/N_0$  (ratio of carrier or bit energy to noise energy) generators and meters, channel emulators, *etc.* However, this method does not include the conversion components.

The prior art testing method suffers from two fundamental disadvantages. First, the method is not capable of evaluating a design at the baseband signaling frequencies since up/downconversion and transmission channel impairments are difficult to separate from algorithmic or other systemic deficiencies. Second, the prior art does not provide a modem-to-conversion and transmission medium evaluation interface prior to integration with actual hardware.

Accordingly, there exists a need for a system and method that allows for the evaluation of a complete transmit modem-to-receive modem system by simulating in the baseband impairments in signal quality manifested during signal conversion and within the transmission medium.

## SUMMARY OF THE INVENTION

The present invention is a system and method that emulates a plurality of signal impairments created by transmission components and media that are employed in typical communication systems. During baseband signal upconversion, transmission through a communication channel, and downconversion, various linear and non-linear distortions manifest themselves in the communication signal. The present invention models a plurality of known linear and non-linear distortions to dynamically evaluate the performance of the transmit and receive modems without requiring empirical evaluation by using a combination of digital hardware and software for accurate, repeatable tests. A variety of linear and non-linear distortion characteristics are impressed on a modulated baseband data signal between transmit and receive modems to effect thorough testing and



optimization of modem performance regardless of the modulation technique and access protocol used and without requiring actual transmission frequency components and/or the physical communication channel medium. The communication transmission emulator comprises transmit modules, receive modules and communication media modules. The transmission emulator is configurable to allow a single channel communication simulation; a full duplex communication simulation; a common base station receiver with multiple users transmitting simulation and a base station transmitter with multiple users receiving simulation, all configurations with or without media impairment modules. The communication media modules simulate multipath signal distortions, signal fading and delay for a chosen medium. Depending upon the simulation, each module type and number can be individually configured, adding a plurality of linear and non-linear impairments to the baseband signal along with delay components to evaluate and optimize modem design.

Accordingly, it is an object of the invention to digitally model the transmission components and communication media of a communication system.

It is another object of the invention to simulate impairments caused by non-ideal transmitters, receivers and communication media in a communications system.

Other objects and advantages of the system and method will become apparent to those skilled in the art after reading the detailed description of the preferred embodiment.

#### BRIEF DESCRIPTION OF THE DRAWINGS

**FIG. 1** is a simplified block diagram of an exemplary, prior art communication system.

**FIG. 2a** is a quadrature modulation constellation pattern with equal amplitude and phase.

**FIG. 2b** is a quadrature modulation constellation pattern showing amplitude imbalance.

**FIG. 2c** is a quadrature modulation constellation pattern showing phase imbalance.

**FIG. 3** is a block diagram of the present invention.

**FIG. 4a** is a block diagram of a single channel transmission simulation using the present invention.

5        **FIG. 4b** is a block diagram of a dual channel transmission with media impairments using the present invention.

**FIG. 4c** is a block diagram of a multi-user, multiple access transmission with media impairments simulation using the present invention.

**FIG. 4d** is a block diagram of a multi-user, multiple access transmission with external radio frequency channel impairments simulation using the present invention.

**FIGs. 5a -5c** is a transmit and receive module linear impairment emulation menu.

**FIG. 6** is a transmit and receive module non-linear impairment emulation menu.

**FIG. 7** is a control signal impairment emulation menu.

**FIGs. 8a and 8b** is a media module impairment emulation menu.

**FIG. 9** is an exemplary plot of  $I$  and  $Q$  signal vectors.

**FIG. 10** is a system diagram of an amplitude imbalance impairment model.

**FIG. 11a** is a system diagram of a carrier phase offset impairment model.

**FIG. 11b** is a plot showing resultant phase impairment.

**FIG. 12** is a system diagram of a carrier phase and frequency offset impairment model.

20        **FIG. 13** is a system diagram of a phase noise impairment model.

**FIG. 14** is a system diagram of a transmit/receive noise impairment model.

**FIG. 15** is a system diagram for a modem noise impairment model.

**FIG. 16** is a system diagram of a group delay impairment model using all pass filters.

**FIG. 17** is a system diagram of a bandwidth limitation impairment model using low pass filters.

**FIG. 18** is a system diagram of a dc carrier offset impairment model.

5 **FIG. 19** is a system diagram of a TPC impairment model.

**FIG. 20** is a system diagram of an AGC impairment model.

**FIG. 21a** is a system diagram of an AM to AM distortion impairment model.

**FIG. 21b** is a plot showing AM to AM distortion components.

**FIG. 21c** is a plot showing amplifier output compression.

**FIG. 22a** is a system diagram of an AM to PM distortion impairment model.

**FIG. 22b** is a plot showing AM to PM distortion components.

**FIGs. 23a and 23b** is a detailed system block diagram of a transmit module of the present invention.

**FIGs. 24a and 24b** is a detailed system block diagram of a receive module of the present invention.

**FIG. 25** is a system diagram of a programmable fading impairment model.

**FIGs. 26a and 26b** is a detailed system block diagram of a communication media module of the present invention.

## DETAILED DESCRIPTION OF THE INVENTION

20 The embodiments will be described with reference to the drawing figures where like numerals represent like elements throughout.

Shown in **FIG. 3** is the communication transmission emulator of the present invention **31**.

The emulator **31** comprises a frame **33** and three addressable, configurable module groups: 1) transmit channel modules **35<sub>1</sub>, 35<sub>2</sub>, ... 35<sub>n</sub>**, 2) receive channel modules **37<sub>1</sub>, 37<sub>2</sub>, ... 37<sub>n</sub>**, and 3) communication channel media modules **39<sub>1</sub>, 39<sub>2</sub>, 39<sub>3</sub>, ... 39<sub>n</sub>**. Each transmit **35<sub>1</sub>, 35<sub>2</sub>, ... 35<sub>n</sub>** and receive **37<sub>1</sub>, 37<sub>2</sub>, ... 37<sub>n</sub>** channel module has an external input **I<sub>I</sub>, I<sub>Q</sub>** and output **O<sub>I</sub>, O<sub>Q</sub>**, respectively, for receiving a signal from or sending to the modulation and demodulation systems of modems under test. Transmit modules **35<sub>1</sub>, 35<sub>2</sub>, ... 35<sub>n</sub>** have an external input for receiving a transmit power control **TPC** signal from the modulation portion of a modem and receive modules have an external input for receiving an automatic gain control **AGC** signal from the demodulation portion of a modem. The data and control signal inputs and outputs are selectable for either analog or digital compatibility. The digital format chosen for the emulator **31** allows for multiplexed or separate *I* and *Q* multi-bit data streams with any modulation scheme that can be modeled in a two-dimensional signal space. For analog signal sources and outputs, the signals will be processed through internal A/D and D/A converters, respectively. The modules may be assembled from discrete components, field programmable gate arrays (FPGAs), application specific integrated circuits (ASICs) and the like.

Each transmit **35<sub>1</sub>, 35<sub>2</sub>, ... 35<sub>n</sub>** and receive **37<sub>1</sub>, 37<sub>2</sub>, ... 37<sub>n</sub>** channel module receives a programmable frequency up **41<sub>1</sub>, 41<sub>2</sub>, ... 41<sub>n</sub>** or down **43<sub>1</sub>, 43<sub>2</sub>, ... 43<sub>n</sub>** converter, respectively, for coupling with an external rf channel communication medium. The operation of the external communication media is beyond the scope of this disclosure.

To accommodate a plurality of modem testing simulations, a virtual splitter/combiner **45** soft couples each resident transmit **35<sub>1</sub>, 35<sub>2</sub>, ... 35<sub>n</sub>**, receive **37<sub>1</sub>, 37<sub>2</sub>, ... 37<sub>n</sub>**, and media **39<sub>1</sub>, 39<sub>2</sub>, 39<sub>3</sub>, ... 39<sub>n</sub>** module in user defined simulations. Since the preferred embodiment of the present invention

31 is modular in construction, the number of modules, module types and rf up/down converters may vary. Alternate embodiments of the invention 31 may have a fixed number of modules and variations in module packaging. The emulator 31 is powered from an ac mains source with the line current reduced by an internal power supply adjustable for the country of use (not shown).

5           A microprocessor 47 with collateral RAM 49 and ROM 51 communicate with each transmit channel 35<sub>1</sub>, 35<sub>2</sub>, ... 35<sub>n</sub>, receive channel 37<sub>1</sub>, 37<sub>2</sub>, ... 37<sub>n</sub>, and communication channel media 39<sub>1</sub>, 39<sub>2</sub>, 39<sub>3</sub>, ... 39<sub>n</sub> module over a dedicated communication bus 53. The microprocessor 47 recognizes and acknowledges each resident module with a unique address. Onboard system configuration is performed by a user navigating through a plurality of menus using a pointing device 55, rotary pulser 57 and switch options with impairment menus displayed on a high resolution 59 display. Coupled to the microprocessor 47 and communication bus 53 is a buffer 61 that is coupled to external connectors 63a,b,c that are sized in matching correspondence with a serial RS-232 point-to-point interface, and IEEE-488 and USB communication buses. Other bus architectures known to those skilled in this art may be used. The communication buses 63a,b,c provide a data highway between the emulator 31 and a user's computer 65, or LAN or WAN (not shown) for remote system configuration.

15           The personal computer 65 running a Mac OS®, Linux®, Windows® or like operating environment executes an installed application program 67 required for remote simulation configuration, emulation module configuration and performance monitoring. The computer 65  
20           handshakes and interrogates the emulator 31 via the communication bus 63c. When launched, the program 67 presents a graphic user interface or GUI 69 allowing a user access to assemble a simulation by choosing the type and number of modules, configure each module and provide soft

interconnection between emulator 31 components. Module configuration comprises choosing specific impairment models for a given module type and entering impairment values by opening a series of dialog boxes having data entry areas and radio buttons, the GUI 69 prompting the user for information that the user fills in as-required. The impairment values are loaded into respective memory locations resident on the modules in-use. The user may also configure the emulator 31 using the command set available from the display 59. To control and provide repeatable tests, various testing simulations and module configuration parameters may be archived for later retrieval.

The application program 67 displays a menu from which the user selects the type and number of modules for a simulation. With the pointing device 55, clicking on a module icon (type) will open a dialog box showing allowable configuration options and a user controlled parameter list. The GUI 69 additionally provides an intuitive method for soft interconnection and configuration of each module type, and after configuration, acts as a control panel for the emulator 31 that visually replicates actual communication hardware. The testing simulation is accompanied with real-time empirical performance measurement and trending.

A user first determines a simulation layout. Example simulations are shown in FIGs. 4a, b, c and d. FIG. 4a shows the emulator 31 coupled to a first modem modulator and to a second modem demodulator simulating a single channel, single access transmission with user selectable impairments in the transmitter and receiver. FIG. 4b shows the emulator 31 coupled to a first modem and a second modem simulating a full duplex, single access transmission with selectable transmitter, receiver and media impairments. FIG. 4c shows a multiple access, multiple user uplink reception simulation between three remote modems communicating with a base station modem with transmitter, receiver and media impairments. FIG. 4d shows a multiple access, multiple user uplink

reception simulation between three remote modems communicating with a base station modem using the programmable frequency rf up/downconverters  $41_1$ ,  $41_2$ ,  $41_3$ ,  $43_1$  with an external air interface emulator for frequency related multiple access protocols. Other simulation configurations are possible.

5           The user assembles the emulator **31** with the various module types that are required for a desired simulation and couples the modems under test to the appropriate inputs and outputs on the transmit channel  $35_1$ ,  $35_2$ , ... $35_n$  and receive channel  $37_1$ ,  $37_2$ , ... $37_n$  modules. Using either the computer **65** or display **59**, the user establishes soft connections with the virtual splitter/combiner **45** between chosen modules completing the simulation configuration.

10           Using the pointing device, the user opens a dialog box for each module type. Transmit  $35_1$ ,  $35_2$ , ... $35_n$  and receive  $37_1$ ,  $37_2$ , ... $37_n$  modules emulate linear and non-linear distortions of mixers, amplifiers, filters, *etc.* Using the pointing device, the user chooses the radio impairments to be included in the simulation and enters the parameter values for each desired impairment model. Opening a filter dialog box used in an impairment type allows the user to specify characteristics and parameters such as type, slope, 3dB points, phase offset, group delay, *etc.* General performance parameters such as sensitivity, *I* and *Q* amplitude, signal levels, sampling frequency, rf bandwidth, filter bandwidths, filter response, and output signal scaling, *etc.*, are selected similarly for each module used in the simulation. Media  $39_1$ ,  $39_2$ ,  $39_3$ , ... $39_n$  channel modules are first configured for the transmission interface type; *i.e.*, unguided or guided, then the configuration parameters for  
20           desired impairment models. The same method is used to enter the configuration parameters for each media module as described above. In this manner, the user assembles a digital emulation of

transmitters, communication media and receivers, encompassing a complete communication path from transmit modem baseband frequencies to receive modem baseband frequencies.

The application program 67 creates a database which allows the user to store setup parameters and emulation data for later use, and assists in the analysis of each simulation by trending chosen parameters. The program also allows the user to dynamically alter a simulation in progress by adjusting the impairments or removing them from the signal path.

Shown in FIGs. 5a, b and c, 6 and 7 are linear, non-linear and control signal menus of the digital impairment models performed in the transmit 35 and receive 37 channel modules. FIGs. 8a and 8b show a menu of the digital impairment models performed in the communication media modules 39.

With reference to FIGs. 5a, b and c, 6 and 7, the emulator allows for either static or dynamic impairment simulations. Impairment values entered in the impairment model variables remain constant unless changed. If a dynamic test is desired for a particular impairment, the impairment variable itself is varied according to predefined waveforms chosen by the user.

Dynamic tests allow for carrier/data recovery and gain control loop response testing since feedback control loops are embedded in typical DSP modem designs. Control loops include carrier phase recovery loops for  $I$  and  $Q$  derotation in coherent demodulation designs, symbol timing recovery, pn code tracking loops in direct sequence spread spectrum systems and automatic gain control (AGC) and transmit power control (TPC) loops. Dynamic tests also simulate impairments commonly found in frequency hopping systems and other discontinuous multiple access modulation protocols such as TDMA, Bluetooth®, *etc.* For example, whenever a frequency hop occurs in a frequency hopping system, the carrier will be at a new frequency and phase, with a new carrier



frequency offset after a brief carrier frequency and phase transient interval. These changes occur periodically in a frequency hopping system and can be simulated with periodic waveforms modulating the impairments, (for example, by modulating the carrier frequency offset impairment variable).

5           Once a communication system is simulated and put through the aforementioned dynamic tests, the response can be observed. For example, a parameter value or metric located in a digital signal processor used in a test modem can be observed, or have its history logged and filed for later retrieval and examination. Observation can also be performed through external test equipment such as storage oscilloscopes and logic analyzers, coupled to the emulator inputs and outputs when performing a simulation which may be used in conjunction with the emulator 31. Parameters such as instantaneous phase error or phase variance, inside of the system's tracking loop, can be measured to evaluate the behavior of a pn-code tracking loop or on an rf carrier derotation control loop in response to the chosen stimulus.

To dynamically verify performance of the various control loops and to check overall modem response, the emulator applies stimuli in the form of impairment variables modulated as sine waveforms, swept sine waveforms, square waveforms for step response testing, sawtooth waveforms for ramp response testing, impulse responses and other composite waveforms known to those skilled in this art. The plurality of waveforms are generated by a processor onboard each transmit and receive module programmed via software and are themselves totally configurable. The dynamic  
20           impairments can be chosen for applicable impairment types from the menus shown on **FIGs. 5a, b and c, 6, and 7a and 7b**. The dynamic simulation measurements are analyzed to verify that the

individual modem control loops achieve their designed performance and to provide further control loop optimization.

## Transmit and Receive Module Linear Impairment Models

### Amplitude Imbalance

- 5 To accurately simulate amplitude imbalance without introducing other adverse affects to the signals, total signal power is altered from a constant value. Shown in **FIG. 9** is a plot of the ideal spacial relationship between  $I$  and  $Q$  signal vectors in a two dimensional signal space and their resultant power vector  $S$ . The sum of vectors  $I$  and  $Q$  yields vector  $S$ . Let  $|S|$  equal the radius of a unit circle (1), then

$$I = S \cos \theta, \text{ and}$$

$$Q = S \sin \theta.$$

Therefore,

$$S = \sqrt{I^2 + Q^2}, \text{ or}$$

$$S^2 = I^2 + Q^2 \quad (3)$$

- 15 yielding,

$$P_{ave} = \frac{1}{T} \int_0^T S^2 dt. \quad (4)$$

Amplitude imbalance is modeled by generating a disparity between the respective  $I$  and  $Q$  signal gains. The emulator models amplitude imbalance as shown in **FIG. 10**. Since total instantaneous power is

$$P_t = |I|^2 + |Q|^2,$$

and in order to keep the total power constant after an imbalance is applied, the  $I$  and  $Q$  imbalances are defined as

$$I(\text{imbalance}) = (\sqrt{2}) \sin(\arctan(10^{(x/10)})), \text{ and} \quad (5)$$

$$Q(\text{imbalance}) = (\sqrt{2}) \cos(\arctan(10^{(x/10)})), \quad (6)$$

where  $\pm x$  controls the imbalance. For  $x = 0$ , the amplitude imbalance between the  $I$  and  $Q$  signals is zero ( $x$  is entered as dB).

#### Carrier Phase Imbalance

Carrier phase imbalance is modeled by inserting a phase difference other than  $90^\circ$  between the  $I$  and  $Q$  signal components. The emulator implements phase imbalance as shown in **FIG. 11a**. Let

$$I = I_a \sin(\omega t), \text{ and}$$

$$Q = Q_a \cos(\omega t), \text{ where}$$

$$|I| \text{ and } |Q| = 1.$$

$I$  and  $Q$  represent examples of signals that are applied to the input of the phase imbalance model with  $A$  equal to the phase error term,

$$I' = I \cos(A) + Q \sin(A)$$

$$= I_a \sin(\omega t) \cos(A) + Q_a \cos(\omega t) \sin(A)$$

$$= \sin(\omega t + A); \text{ and} \quad (7)$$

$$\begin{aligned} Q' &= Q \cos(A) + I \sin(A) \\ &= Q_a \cos(\omega t) \cos(A) + I_a \sin(\omega t) \sin(A) \\ &= \cos(\omega t - A). \end{aligned} \quad (8)$$

5 **FIG. 11b** shows the effect of the phase imbalance error term  $A$  on the  $I$  and  $Q$  vectors.

#### Carrier Frequency Offset and Carrier Phase Offset

Frequency offset is modeled by altering the frequency of the carrier signal by constantly rotating the  $I$  and  $Q$  signals with a frequency offset signal  $X$ . The emulator implements frequency offset as shown in **FIG. 12**.

$$\begin{aligned} I &= I_a \sin(\omega t), \text{ and} \\ Q &= Q_a \cos(\omega t), \\ I' &= I \cos(A) + Q \sin(A) \\ &= I_a \sin(\omega t) \cos(A) + Q_a \cos(\omega t) \sin(A) \\ &= \sin(\omega t + A) \\ &= \sin((\omega + X) t), \text{ and} \end{aligned} \quad (9)$$

$$\begin{aligned} Q' &= Q \cos(A) - I \sin(A) \\ &= Q_a \cos(\omega t) \cos(A) - I_a \sin(\omega t) \sin(A) \\ &= \cos(\omega t + A) \\ &= \cos((\omega + X) t). \end{aligned} \quad (10)$$

The phase offset component is  $A$  and the frequency offset component is  $X$  and  $|I|$  and  $|Q| = 1$ .

### Carrier Phase Noise

Carrier phase noise is modeled by introducing a random noise component to each signal.

5 The emulator implements phase noise as shown in **FIG. 13**. A pseudo noise (pn) generator having a Gaussian distribution adds noise similar to that generated in semiconductors.

$$pn \text{ signal} = A = x(t) \text{ with } |I| \text{ and } |Q| = 1$$

where  $x(t)$  is a random variable with a Gaussian distribution, and

$$I = I_a \sin(\omega t), \text{ and}$$

$$Q = Q_a \cos(\omega t),$$

$$I' = I \cos(A) + Q \sin(A)$$

$$= I_a \sin(\omega t) \cos(A) + Q_a \cos(\omega t) \sin(A)$$

$$= \sin(\omega t + A), \text{ and} \quad (11)$$

$$Q' = Q \cos(A) - I \sin(A)$$

$$= Q_a \cos(\omega t) \cos(A) - I_a \sin(\omega t) \sin(A)$$

$$= \cos(\omega t + A). \quad (12)$$

The  $I$  and  $Q$  signals will be constantly phase modulated by the pn signal  $A$ .

### Transmit/Receive Noise

Transmit (or receive) noise is modeled by introducing a random noise component to the noise floor of each  $I$  and  $Q$  signal. The emulator implements transmit/receive noise as shown in **FIG. 14**. An additive white Gaussian noise source adds a miscellaneous noise component.

#### Modem Noise

- 5        Modem noise is modeled by introducing a random noise component to each  $I$  and  $Q$  signal. The emulator implements transmit/receive noise as shown in **FIG. 15**. An additive white Gaussian noise source adds a miscellaneous noise component.

#### Bandwidth Group Delay

Bandwidth group delay variation is modeled with pass band filters having a flat response but with variable phase. The emulator implements group delay as shown in **FIG. 16**.

#### Bandwidth Limitations

Bandwidth limitations are modeled using a finite impulse response (FIR) filter, infinite impulse response (IIR) or combination filter to create variable cut-off frequencies and manipulate frequency response. The emulator implements bandwidth limitations as shown in **FIG. 17**.

#### Carrier Leakage

Carrier leakage is modeled by adding a Vdc offset component to the  $I$  and  $Q$  signals. The emulator implements carrier imbalance as shown in **FIG. 18**.

#### Control Signal

- 20        Control signal impairments are modeled by first converting a log-gain control signal to a linear-gain signal and secondly, processing through a non-linearity function shown in **FIGs. 19 and 20**.

## Transmit and Receive Module Non-Linear Impairment Models

### Amplitude Modulation (AM) to Amplitude Modulation (AM)

AM to AM distortion is modeled using a programmable coefficient of non-linearity. The emulator implements AM to AM distortion as shown in **FIG. 21a**. The resultant signal constellation is shown in **FIG. 21b**. The outer perimeter of the box represents an uncompressed  $I/Q$  vector moving through different phase angles with constant amplitude. When impairment variable  $k$  is changed to a value compressing the signal, the vector length is shortened. This action creates the smaller 'television screen' shaped box within. As the magnitude of the vector changes, the phase remains constant. This impairment is implemented using Equation 15 as shown in **FIG. 20a**, or, in an alternative embodiment, as a look-up-table (LUT). The LUT implements the Equation, or, contains a predetermined set of values to allow modeling of any arbitrary AM-AM distortion.

$$I' = I (1 - k (I^2 + Q^2)), \text{ and} \quad (13)$$

$$Q' = Q (1 - k (I^2 + Q^2)), \quad (14)$$

where

$$I = A \sin (\omega t)$$

$$Q = A \cos (\omega t), \text{ where } A \text{ is a constant.}$$

Since  $I^2$  is approximately equal to  $Q^2$ , Equations 13 and 14 reduce to the following form:

$$I' = I - 2kI^3. \quad (15)$$

A plot of this equation is shown in **FIG. 21c** for  $k = 1/12$ ,  $I = 0 - 1.5$ . This is a typical amplifier compression curve.

## Amplitude Modulation (AM) to Phase Modulation (PM)

AM to PM distortion is modeled using a programmable coefficient of non-linearity. The emulator models AM to PM distortion as shown in **FIG. 22a** where  $k$  is the programmable coefficient of non-linearity. The constellation shown in **FIG. 22b** represents a signal vector with no impairment rotating through different phases. As the input signal level increases, the vector will increase in magnitude and start to rotate in a counter-clockwise direction, indicating an increase in phase. If the AM-AM impairment is off, there will be no change in length. This impairment is implemented using Equations 17 and 18 as shown in **FIG. 22a**, or, in an alternative embodiment, as a LUT. The LUT implements the Equations, or, contains a predetermined set of values to allow modeling of any arbitrary AM-PM distortion.

$$A = (I^2 + Q^2)^2 / k, \quad (16)$$

$$I = 1 \sin(\omega t), \text{ and}$$

$$Q = 1 \cos(\omega t)$$

then,

$$\begin{aligned} I' &= I \cos(A) + Q \sin(A) \\ &= \sin(\omega t) \cos(A) + \cos(\omega t) \sin(A) \\ &= \sin(\omega t + A), \text{ and} \end{aligned} \quad (17)$$

$$\begin{aligned} Q' &= I \sin(A) - Q \cos(A) \\ &= \sin(\omega t) \sin(A) - \cos(\omega t) \cos(A) \\ &= -\cos(\omega t + A). \end{aligned} \quad (18)$$



The impairment variable **C** in **FIG. 22a** controls the amount of insertion phase variation affecting the signal. The phase is offset by an amount proportional to the TPC signal level.

Shown in **FIGs. 23a** and **23b**, and **24a** and **24b** is the execution of the above impairment models by the present invention **31** transmit channel **35** and receive channel **37** modules respectively.

5 Referencing **FIGs. 23a** and **23b**, data signal outputs and a total power control (TPC) output from a transmit modem (not shown) assembled in a simulation are coupled to the signal **I<sub>I</sub>**, **I<sub>Q</sub>** and **TPC** inputs of a transmit module **35**. The inputs **I<sub>I</sub>**, **I<sub>Q</sub>** are coupled to a signal conditioner **101** for level adjustment or digitization if the input signal **I<sub>I</sub>**, **I<sub>Q</sub>** is in continuous time. The signal **103** is output to a filter **105** which performs a high pass filter action blocking low frequency signal components. The filtered signal **107** is coupled to an addition processor **109** where the Gaussian noise addition (as modem noise) and carrier leakage impairments are performed. Gaussian noise is generated by an onboard generator **111** having an output **113** coupled to the addition processor **109** and to a phase noise generator **115**. After addition impairments are performed, the output **117** is coupled to a filter **119** which performs the bandwidth limitation impairment.

10 The output **121** of the filter **119** is coupled to a rotation processor **123** which performs *I* and *Q* rotation on symbol components. The rotation processor **123** applies a fixed angular rotation between the *I* and *Q* components to effect phase imbalance; a continuous rotation to effect frequency offset; a static rotation to effect phase offset; and a random  $\pm$  change in phase angle to effect phase noise. The rotation processor **123** output **125** is coupled to a phase shifter **127** which applies the non-linear AM to PM and insertion phase variation impairments and is output **129** to a final gain processor **131** which performs the gain imbalance, AM to AM impairment and output signal conditioning. A power measurement processor **151** calculates the instantaneous signal power ( $I^2 +$

20

$Q^2$ ) **153** for use by the gain processor **131** to perform the AM-AM and AM-PM impairments. The power measurement processor **151** also calculates the long term average power (of  $I^2 + Q^2$ ) for display **59**, **69**.

A stimulus generator **133** for producing the plurality of wave shapes **135** for dynamic evaluation is coupled to the addition processor **109**, rotation processor **123** and gain processor **131**. Additionally, a waveform generator **137** generates signals **139** for calibration and module self diagnostics and demonstration purposes. Depending upon the simulation in progress, the impaired digital output  $i_{I1}$ ,  $i_{Q1}$  is coupled to an rf up-converter **41** for external channel emulation, or coupled to a media **39** or receive **37** module via the virtual splitter/combiner **45**. Performance measurements are captured by a signal monitor **141** and waveform capture **143** for outputting onto the communication bus **53**. A bus control interface **145** bidirectionally communicates with the central microprocessor **47** via the communication bus **53** downloading impairment variables and uploading performance measurements for display or storage.

With architecture and function similar to the transmit module **35**, the receive module **37** shown in **FIGs. 24a** and **24b** accepts digital signals output from a transmit **35**  $i_{I1}$ ,  $i_{Q1}$  or media **39**  $i_{I2}$ ,  $i_{Q2}$  module via the virtual combiner/splitter **45**, or by rf down-converter **43** depending upon a given simulation. The digital input signals are coupled to a signal conditioner **201** and output **203** to an addition processor **205** where the Gaussian noise and carrier dc offset impairments are performed. Gaussian noise is generated by an onboard generator **207** having an output **209** coupled to the addition processor **205** and to a phase noise generator **211**. The addition processor output **213** is coupled to a filter **215** for performing the bandwidth limitation impairment. The filter output **217** is coupled to a rotation processor **219** which performs phase offset, phase noise and frequency offset

and phase imbalance impairments. After rotation **221**, a gain processor **223** performs AM-AM and gain imbalance impairments. The final process in the signal flow **225** is a filter emulator **227**.

A power measurement processor **243** calculates the instantaneous signal power ( $I^2 + Q^2$ ) **245** for use by the gain processor **223** to perform the AM-AM impairment. The power measurement processor **243** also calculates the long term average power (of  $I^2 + Q^2$ ) for display **59**, **69**. A stimulus generator **229** for producing the plurality of wave shapes **231** for dynamic evaluation is coupled to the addition **205**, rotation **219** and gain **223** processors. The impaired signal is output  $O_I$ ,  $O_Q$  to a receive modem demodulator (not shown). Performance measurements are captured by a signal monitor **235** and waveform capture **237** for outputting onto the communication bus **53**. A bus control interface **233** bidirectionally communicates with the central microprocessor **47** via the communication bus **53** downloading impairment variables and uploading performance measurements for display or storage.

Shown in **FIGs. 8a** and **8b**, media channel impairment modules **39** emulate the impairments of the transmission environment and nature of an application. Some simulations may be noisier than others or have noise attributes (impulse, fixed frequency, *etc.*) that others do not; some media impairments are not a factor in all situations. For example, a fixed point to point communication system is less affected by multipath and fading than a fast moving cellular service endures when changing location while maintaining contact with several base stations.

The media modules **39** are implemented using either a general purpose processor or a digital signal processor, or a plurality of processors. The use of digital signal processing is known to those skilled in this art. The plurality of media emulation models are stored in collateral memory with adjustment variables accessed in the same manner as the transmit **35** and receive **37** modules. Media

5 impairments for unguided applications comprise value fluctuation such as envelope curve fading, (Rayleigh, Ricean, Nakagami, log-normal distributions and others), random FM noise, and dispersion of delay, location characteristics (logarithmic fluctuation) and distance characteristics models (Okumura, Longley-Rice and others). Impairments for guided applications comprise attenuation and delay.

Radio waves in mobile communications are subject to reflection, diffraction and scattering caused by the impact of obstacles and reflecting objects such as buildings, trees and undulations in the terrain. The resultant multiple radio waves traveling different paths can interfere with each other, causing dramatic fluctuations in signal strength. This is known as fading.

Rayleigh and Ricean models characterize the multipath phenomena that leads to intersymbol interference (ISI) caused by relative path delay. Since some of the waves arrive later than others, one version of an early data bit can actually arrive at the receiver simultaneously with or even after a later data bit that came by a shorter, more direct path. Therefore, the first bit interferes with subsequent bits. This form of ISI has a different underlying cause than ISI caused by time-smearing of bits as they pass through a bandwidth-limited channel.

Some communication media exhibit slow fading with propagation drifts over a time period of many symbols or bits, which results from atmospheric changes or topography-based change. Others have fast fading, with fast transitions during one bit period or frame period and which can corrupt a string of consecutive symbols. Neither fast nor slow fading is the same as impulse noise from lightning or the like.

## Media Module Impairment Models

### Rayleigh Fading

For unguided applications, Rayleigh-noise-distribution is modeled by emulating the multiple independent paths a signal transmitted to a receiver experiences. The receiver sees constructive and destructive interference as the sum of numerous random variables. The typical received signal clusters around a most likely value, but the signal at any time deviates narrowly or widely from this most likely value, depending on the programmable statistics of median point and number of interference waves in the channel model. The range and likelihood of deviation approximate the fading channel observed in mobile communications. Rayleigh fades of 10 to 30 dB are common and can range to 50 dB. The emulator implements fading impairments as shown in **FIG. 25**. A signal enters a tapped delay line having variable time delays  $\tau$  with each tap output to an independent complex Gaussian modulator **GM**, a zero mean, log-normal modulator **LN** and a weighting factor **W** to produce the plurality of fading models. Each tap representing a multipath component. Each complex modulator may also include a plurality of delay taps, allowing for a plurality of delay profiles.

### Ricean Fading

Ricean fading is modeled by contouring Rayleigh fading as a more realistic situation. This model emulates a strongly dominant path, typically a line-of-sight path in conjunction with multiple random paths. The Rayleigh model is usually associated with outdoor propagation, whereas the Ricean model is more closely associated with indoor paths.

## Log Normal Distribution

Log normal models emulate a slow variation in field strength, which can occur when a receiver moves steadily away from a signal source or when there is a temporary physical block, such as a hill between a transmitter and a receiver. The blockage adds a less-than-unity coefficient to the path loss and is identified mathematically as log-normal fading.

## Noise

FM noise is modeled by introducing a random noise component to each signal and the maximum Doppler frequency. A Gaussian noise source adds noise similar to that manifested during transmission. The amount of carrier-frequency shifting that a receiver observes is a function of both the relative speed between source and receiver and the nominal carrier frequency. The Doppler shift is approximately 1 kHz in most earthbound applications but can be far greater for satellite links, which have higher relative velocities.

## Delay Dispersion

Delay dispersion is modeled by choosing different frequencies and delays to compensate for different frequencies having different velocities thereby having differing times of arrival.

## Location Characteristics

Models for statistical topography simulation for modeling structures and topography lying between a user and base station. Variables defining median point and standard deviation over a predefined interval for the statistical model emulate differing operating environments.

## Distance Characteristics

Distance characteristics are statistical approaches such as Okumura's distance curve which consider antenna height, distance between communication stations and operating carrier

frequency to arrive at a model of radio wave propagation in terrestrial mobile communications. From the distance characteristics location characteristics can be obtained using normal logarithmic fluctuation along with instantaneous value fluctuation (such as Rayleigh fading).

5            Guided media has many degradations and inconsistencies. For guided communication links, attenuation and dispersion, rather than noise and other time-variant behaviors, are the dominant channel model.

#### Attenuation

Attenuation is modeled by a digital attenuator or variable gain amplifier.

#### Delay

Delay is modeled in unguided media as a variable representing different frequencies having different velocities. In guided media, delay is modeled as the architectural profile of a cable or waveguide.

Shown in **FIGs. 26a** and **26b** is the execution of the above media impairment models by the  
15    present invention **31** media channel module **39**. A media module **39** accepts the digital signal output  $i_{I1}$ ,  $i_{Q1}$  from a transmit module **35** via the virtual combiner/splitter **45** depending upon a given simulation. The digital input  $i_{I1}$ ,  $i_{Q1}$  signals are coupled to a signal conditioner **301** and are output **303** to a programmable value fluctuation processor **305** which implements the plurality of fading models. Each fading model, *i.e.* Rayleigh, Ricean, log-normal, *etc.*, is downloaded from onboard  
20    memory (not shown) into the processor **305** for a given simulation. After characterizing fading, the

output 307 is coupled to an addition processor 309 where the Gaussian noise impairment is performed. Gaussian noise is generated by an onboard generator 311. The addition processor 309 output 313 is coupled to an environment processor 315 for performing the topography and distance impairments. The various distance curves are maintained in resident memory. The environment processor 315 output 317 is coupled to a gain processor 319 for attenuation impairments. The gain processor 319 output 321 is coupled to a delay processor 323 for performing the delay impairments. A stimulus generator 325 for producing the plurality of wave shapes 327 for dynamic evaluation is coupled to the value fluctuation 305, addition 309, environment (distance) 315, gain 319 and delay 323 processors. The same waveforms 327 can be applied to each delay length  $\tau$  and weighting factor  $W$  verifying symbol timing recovery loops, pn tracking loops, pn acquisition performance and behavior, and rake finger multipath searches, detection, and tracking performance. The signal is output  $i_{I2}, i_{Q2}$  to a receive module 37. Performance measurements are captured by a signal monitor 329 and waveform capture 331 for outputting onto the communication bus 53. A bus control interface 333 bidirectionally communicates with the central microprocessor 47 via the communication bus 53 downloading impairment variables and uploading performance measurements for display or storage.

The present invention 31 is taught as a modular approach for assembling a complete transmission simulation from source modem to destination modem regardless of what modulation technique or access protocol is used or any combination thereof. Embodiments of the invention that have a fixed number and type of modules can be appreciated by one skilled in this art.



While the present invention has been described in terms of the preferred embodiment, other variations which are within the scope of the invention as outlined in the claims below will be apparent to those skilled in the art.

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## CLAIMS

What is claimed is:

1. An emulator for use in digital communication design, optimization, and validation comprising:

a transmitter impairments emulation;

a transmitter interface;

a receiver impairments emulation; and

a receiver interface.

2. An emulator for modeling a plurality of signal impairments which manifest themselves onto a communication data signal between modulation and demodulation in a communication system, the emulator comprising:

a plurality of programmable transmit modules emulating a plurality of signal impairments that occur during communication media modulation, each said transmit module having a signal input for coupling with a transmit modem, an output and a communication bus coupling;

a plurality of programmable receive modules emulating a plurality of signal impairments that occur during communication media demodulation, each said receive module having a signal input, an output for coupling with a receive modem and a communication bus coupling;

a user interface communicating with a processor for defining a communication system simulation by soft coupling a required number of said transmit and receive modules in a user defined simulation arrangement using said transmit module outputs and said receive module inputs; and

said processor instructing each of said required modules via a communication bus to use specific signal impairments from said plurality of respective signal impairments for said user defined simulation arrangement.

3. The emulator according to claim 2 wherein said plurality of transmit module impairments further comprise linear impairments.

4. The emulator according to claim 3 wherein said transmit module linear impairments further comprise:

amplitude imbalance;  
bandwidth group delay;  
bandwidth limitations;  
carrier frequency offset;  
carrier leakage;  
carrier phase imbalance;  
carrier phase noise;  
carrier phase offset  
modem noise; and  
transmitter noise.

5. The emulator according to claim 4 wherein said plurality of transmit module impairments further comprise non-linear impairments.

6. The emulator according to claim 5 wherein said transmit module non-linear impairments further comprise:

amplitude modulation to amplitude modulation; and  
amplitude modulation to phase modulation.

7. The emulator according to claim 6 wherein said plurality of receive module impairments further comprise linear impairments.

8. The emulator according to claim 7 wherein said receive module linear impairments further comprise:

amplitude imbalance;  
bandwidth group delay;  
bandwidth limitations;  
carrier frequency offset;  
carrier dc offset;  
carrier phase imbalance;  
carrier phase noise;  
carrier phase offset  
modem noise; and  
receiver noise.

9. The emulator according to claim 8 wherein said plurality of receive module impairments further comprise non-linear impairments.

10. The emulator according to claim 9 wherein said receive module non-linear impairments further comprise:

amplitude modulation to amplitude modulation; and  
amplitude modulation to phase modulation.

11. The emulator according to claim 10 further comprising a plurality of programmable media modules emulating a plurality of signal impairments that occur during signal transmission through a communication channel media, each said media module having a signal input for coupling with the output of a transmit module, a signal output for coupling with the input of a receive module and a communication bus coupling for coupling with said communication bus.

12. The emulator according to claim 11 wherein said media module emulates guided and unguided communication channel media impairments.

13. The emulator according to claim 12 wherein said guided media impairments comprise attenuation and delay.

14. The emulator according to claim 13 wherein said unguided media impairments comprise value fluctuation, location characteristics and distance characteristics.

15. An emulator for modeling at least one signal impairment which is manifest onto a communication data signal between modulation and demodulation in a communication system, the emulator comprising:

at least one programmable transmit module emulating at least one signal impairment that occurs during communication media modulation, said at least one transmit module having a signal input for coupling with a transmit modem, an output and a processor interface;

at least one programmable receive module emulating at least one signal impairment that occurs during communication media demodulation, said at least one receive module having a signal input, an output for coupling with a receive modem and a processor interface; and

a processor, with associated memory, for configuring said transmit and receive modules via selected characteristics stored in memory.

16. The emulator of claim 15 wherein said at least one transmit module impairment emulates a dynamic impairment.

17. The emulator of claim 16 wherein said dynamic impairment comprises at least one of the following of sine waveform; swept sine waveform; square waveform; sawtooth waveform; or impulse waveform.

18. The emulator of claim 15 wherein said at least one receive module emulates a dynamic impairment.

19. The emulator of claim 18 wherein said dynamic impairment comprises at least one of the following of sine waveform; swept sine waveform; square waveform; sawtooth waveform; or impulse waveform.

20. The emulator of claim 15 further comprising at least one programmable media module emulating at least one signal impairments that occur during signal transmission through a communication channel media, each said media module having a signal input for coupling with the output of a transmit module, a signal output for coupling with the input of a receive module and a processor interface.

21. The emulator of claim 15 wherein said at least one transmit module impairment further comprises a linear impairment.

22. The emulator according to claim 16 wherein said transmit module linear impairments further comprise at least one of the following of amplitude imbalance; bandwidth group delay; bandwidth limitations; carrier frequency offset; carrier leakage; carrier phase imbalance; carrier phase noise; carrier phase offset; modem noise; or transmitter noise.

## ABSTRACT OF THE DISCLOSURE

A communication transmission emulator digitally emulates a plurality of signal impairments created by the transmission components and communication medium in a typical communication system. A variety of linear and non-linear distortion characteristics are impressed on a baseband signal between transmit and receive modems to effect thorough testing and optimization of modem performance without requiring transmission frequency components and/or the communication channel. The communication transmission emulator comprises transmission channel transmit modules, receive modules and communication media modules. Transmit and receive modules accepting or outputting analogue or digital signals. The transmission emulator is configurable to allow a simulation of a single channel communication; a simulation of a full duplex communication; a simulation of a common base station receiver with multiple users transmitting and a simulation of a base station transmitter with multiple users receiving, all configurations with or without the communication media emulator module. The communication media modules simulate multipath signal components and distortions for the chosen medium. In dependence upon the configuration, each module type and number can be individually configured, adding a plurality of linear and non-linear impairments to the baseband signal along with delay components to evaluate and optimize modem design and technology.



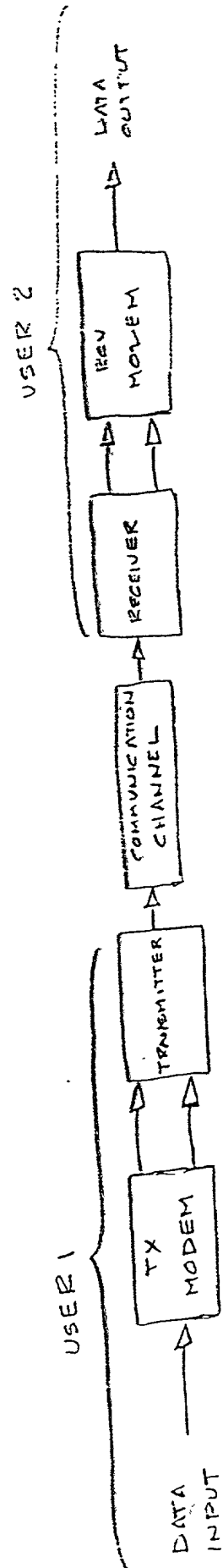
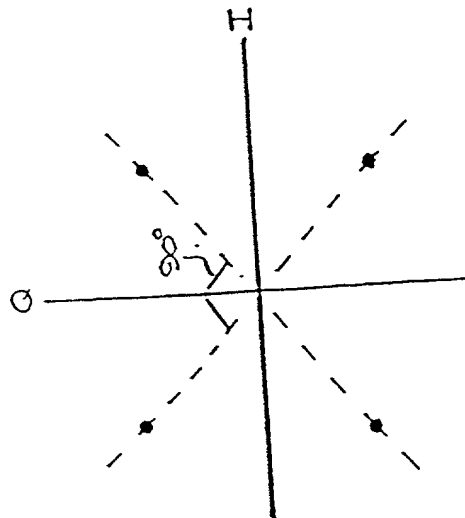
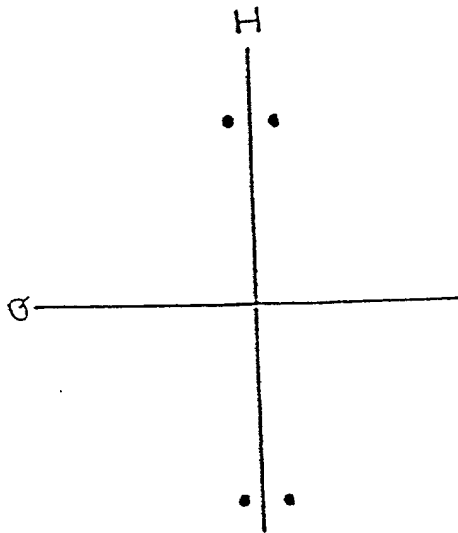


FIG. 1  
PRIOR ART



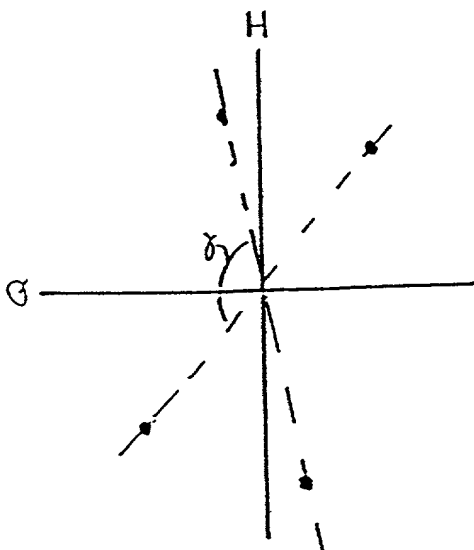
BALANCED

FIG. 2d



AMPLITUDE  
IMBALANCE

FIG. 2b



PHASE  
IMBALANCE

FIG. 2c

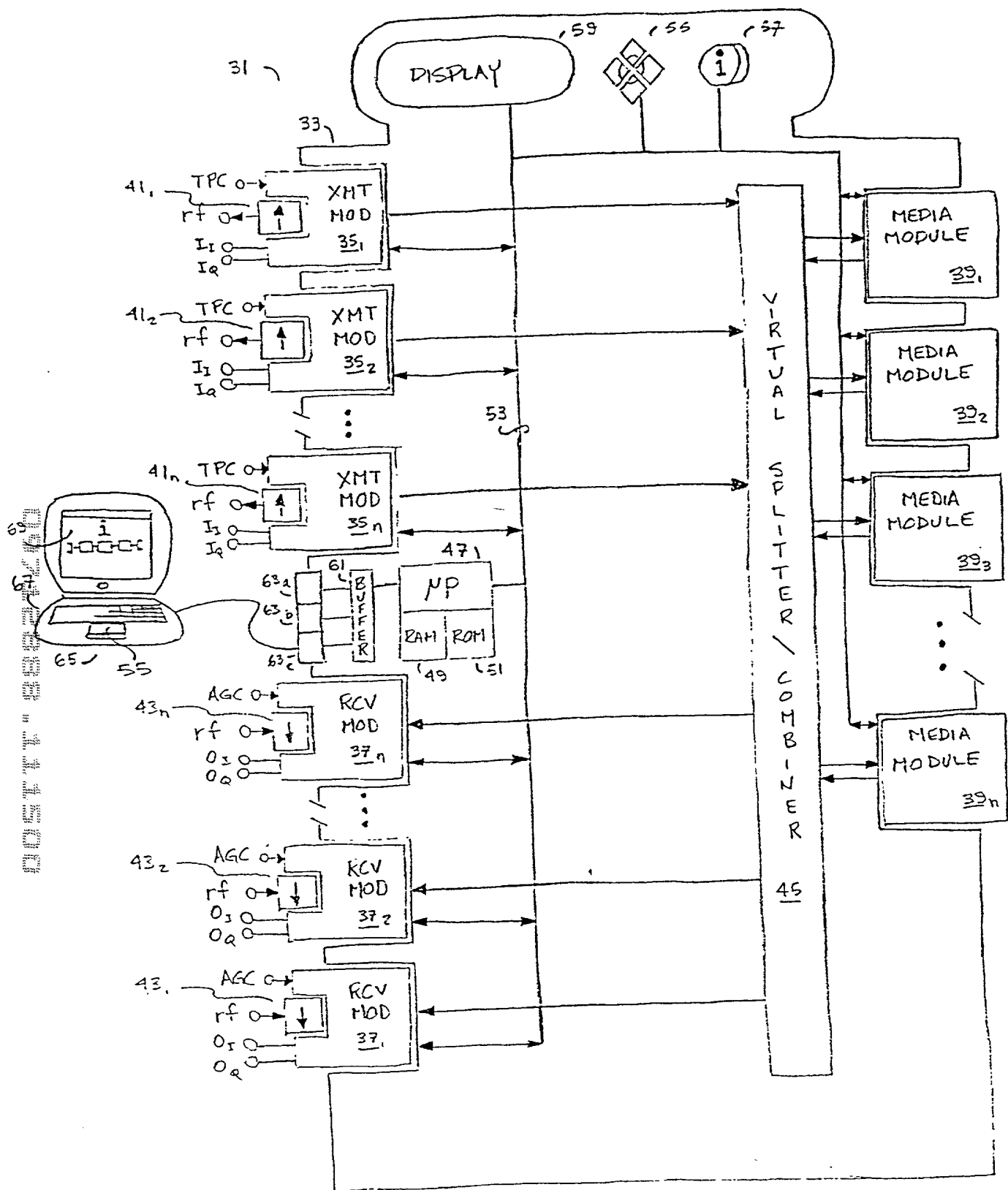


FIG. 3



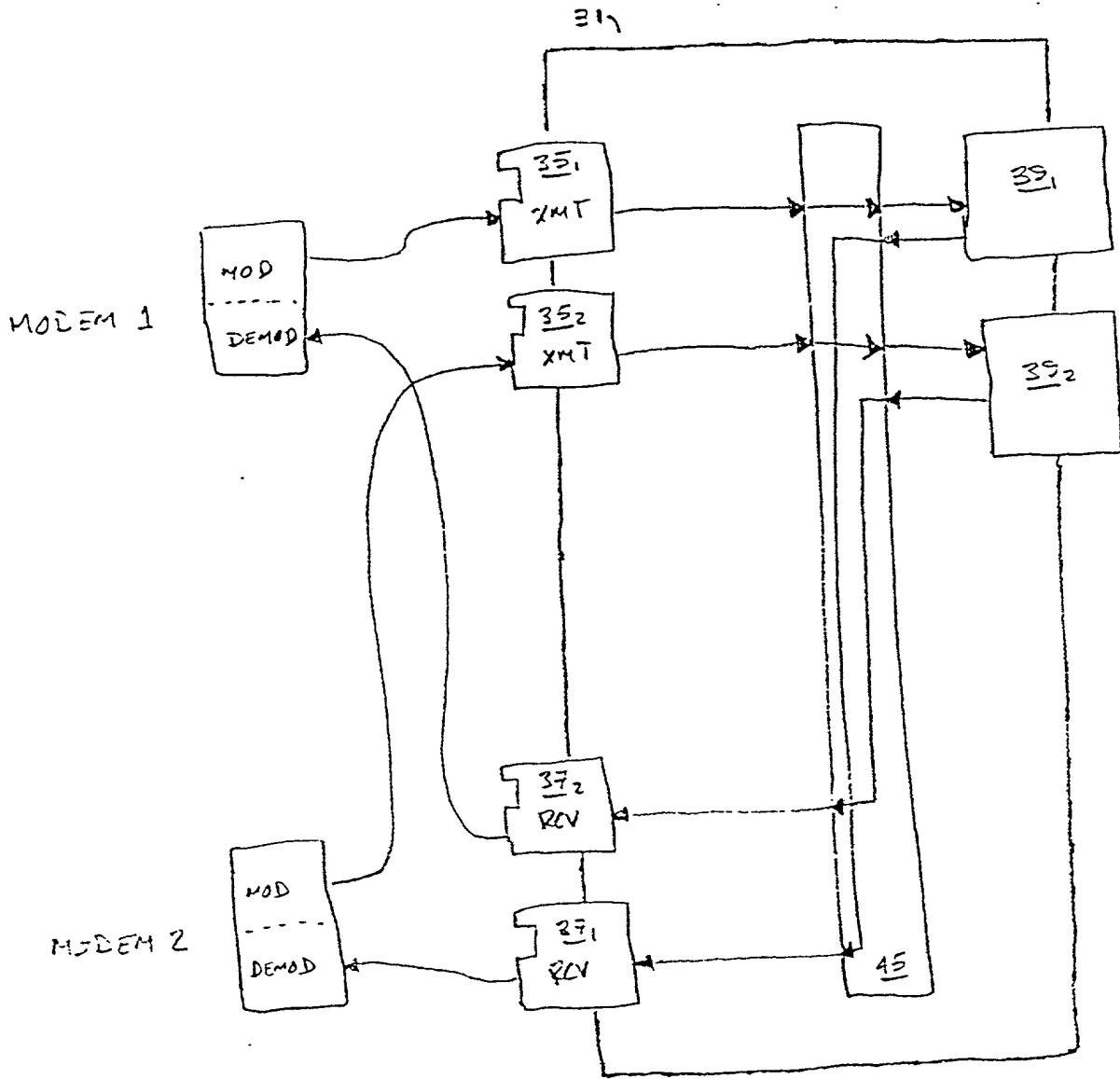


FIG. 4b

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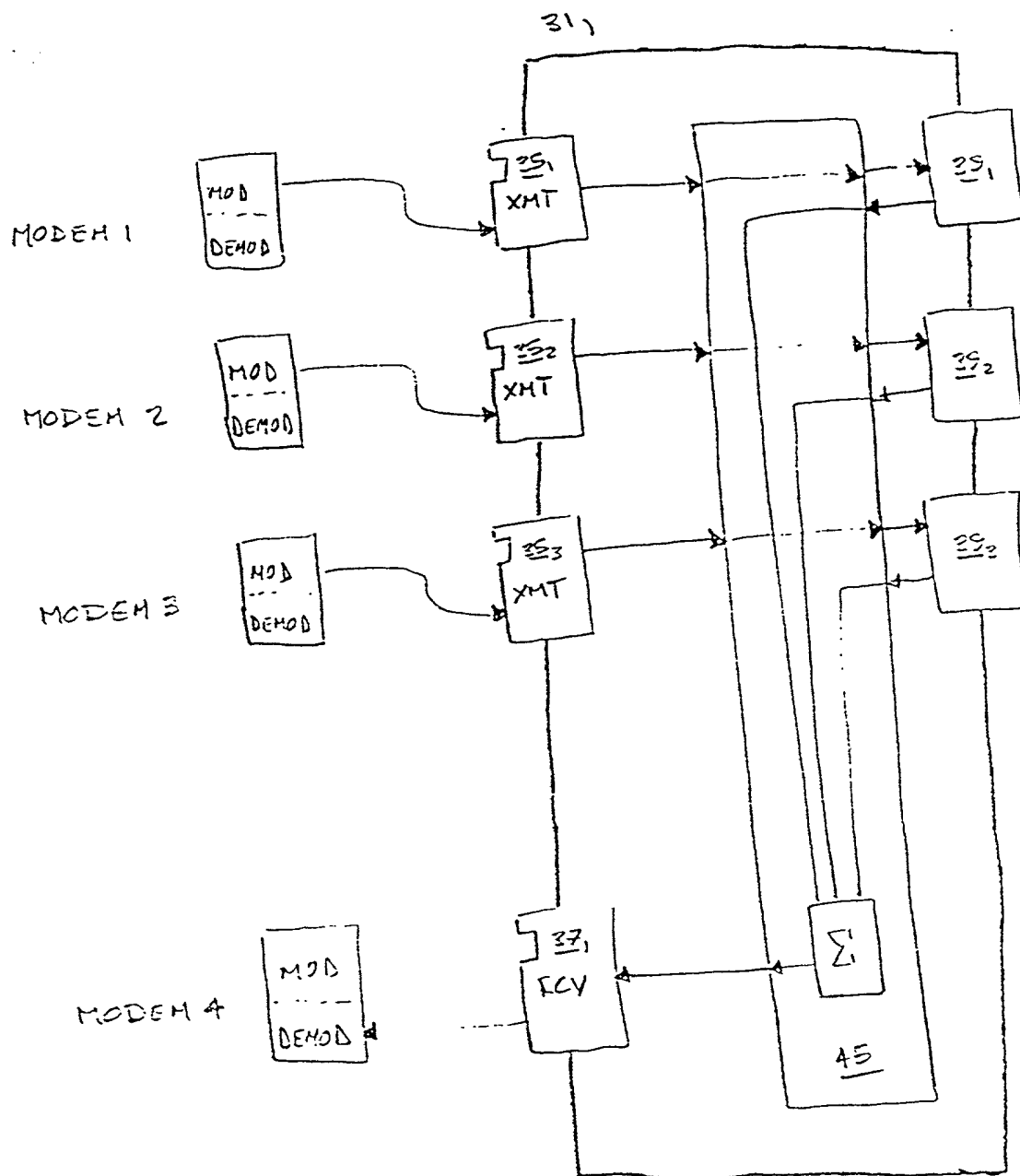


FIG. 4c

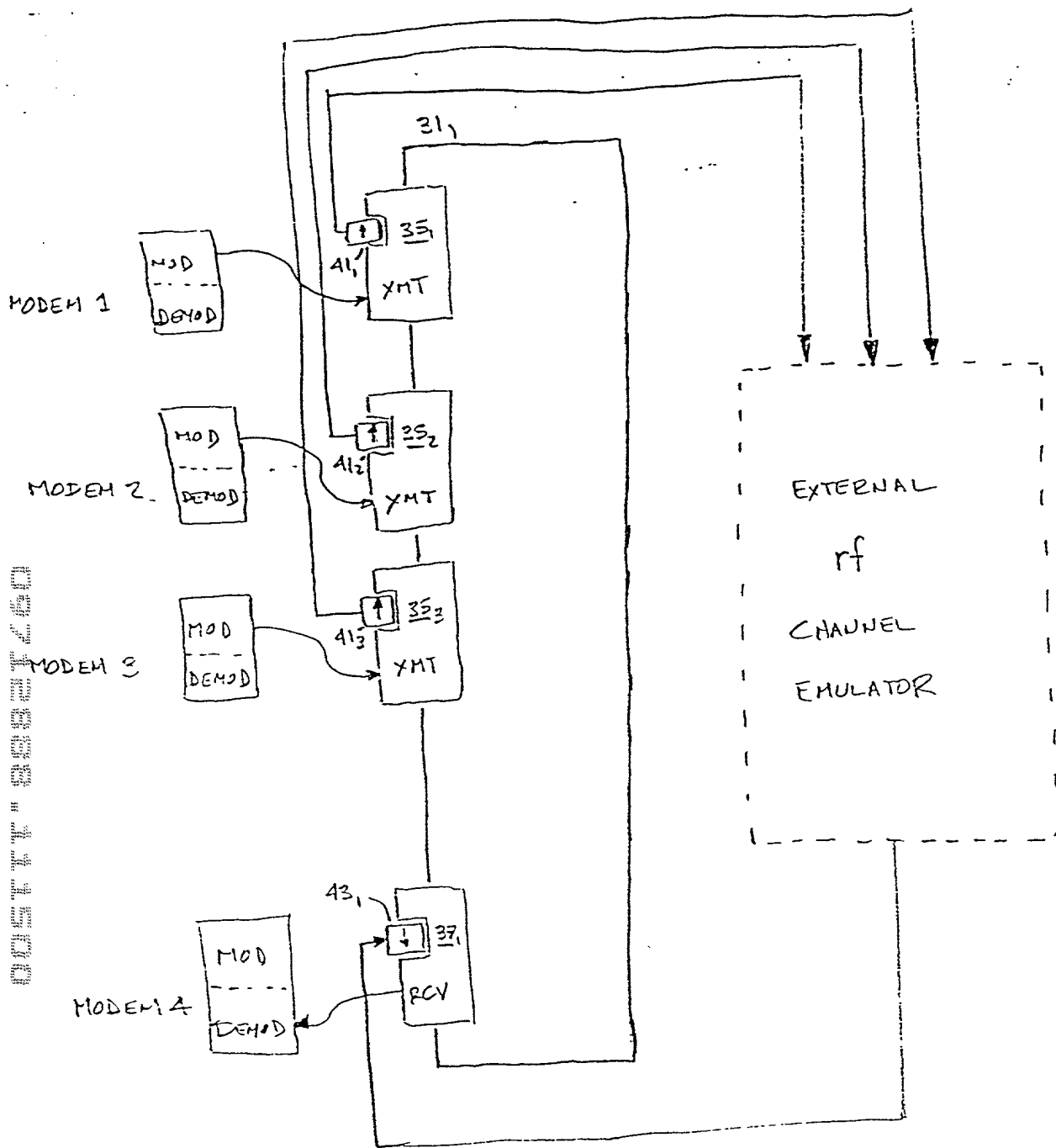


FIG. 4d

# LINEAR IMPAIRMENTS MENU

AMPLITUDE

IMBALANCE (VARIABLE % IN DB)

DYNAMIC

- N
- SWEEP N
- $\pi$
- $\Lambda$
- 1

BANDWIDTH

GROUP DELAY (VARIABLE IN  $\mu$ S V. HZ)

BANDWIDTH LIMITATIONS (VARIABLE IN DB V. HZ)

- LPF
- HPF (dc blocking)

CARRIER

FREQUENCY OFFSET (VARIABLE % IN HZ)

DYNAMIC

- N
- SWEEP N
- $\pi$
- $\Lambda$
- 1

dc OFFSET (RECEIVER) (VARIABLE IN dBm)

DYNAMIC

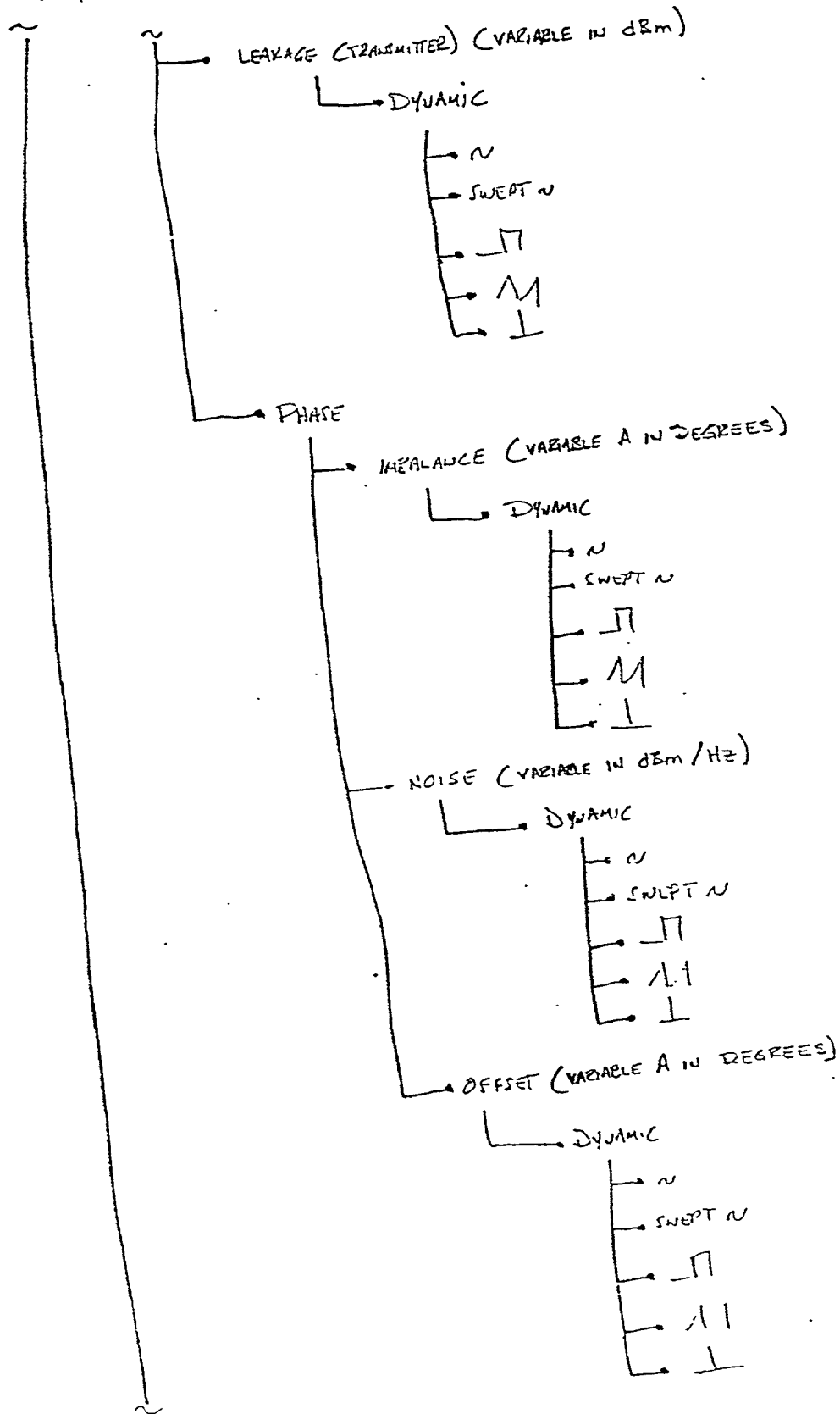
- N
- SWEEP N
- $\pi$
- $\Lambda$
- 1

TO FIG. 5b

FIG. 5c



FROM FIG. 5a



TO FIG. 5c

FIG. 5b

FROM FIG. 5b

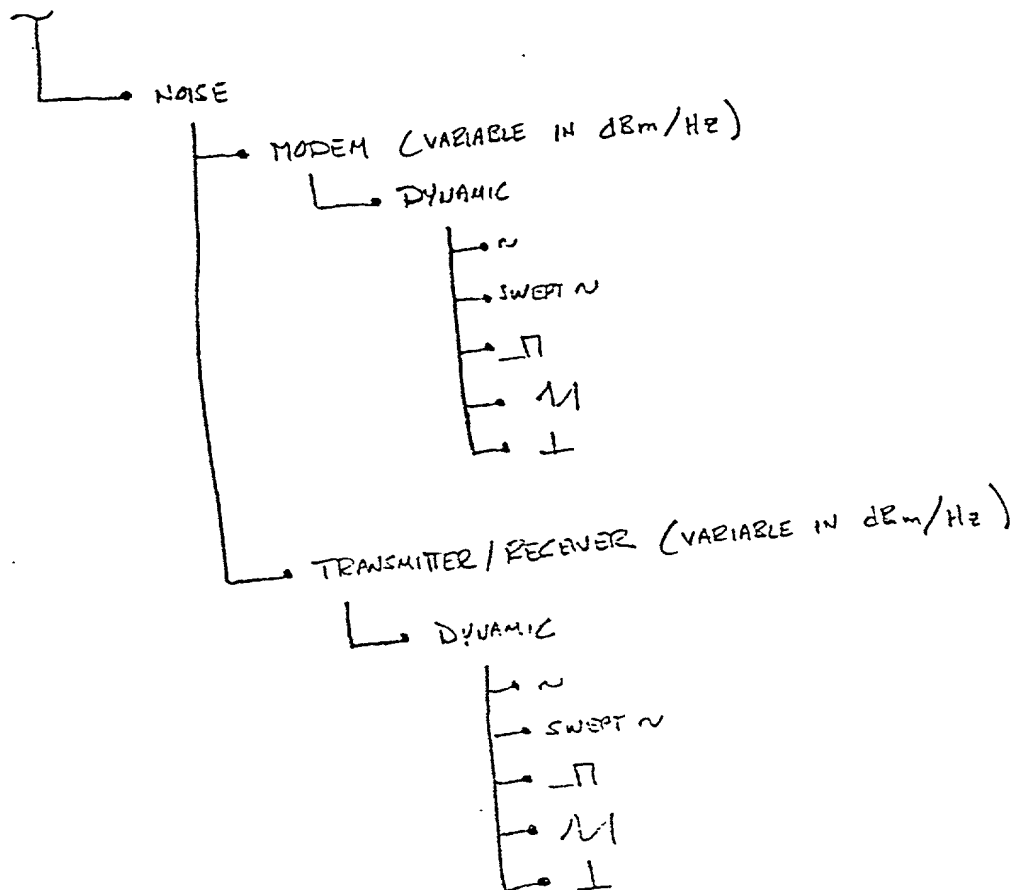


FIG. 5c

## NON-LINEAR IMPAIRMENTS MENU

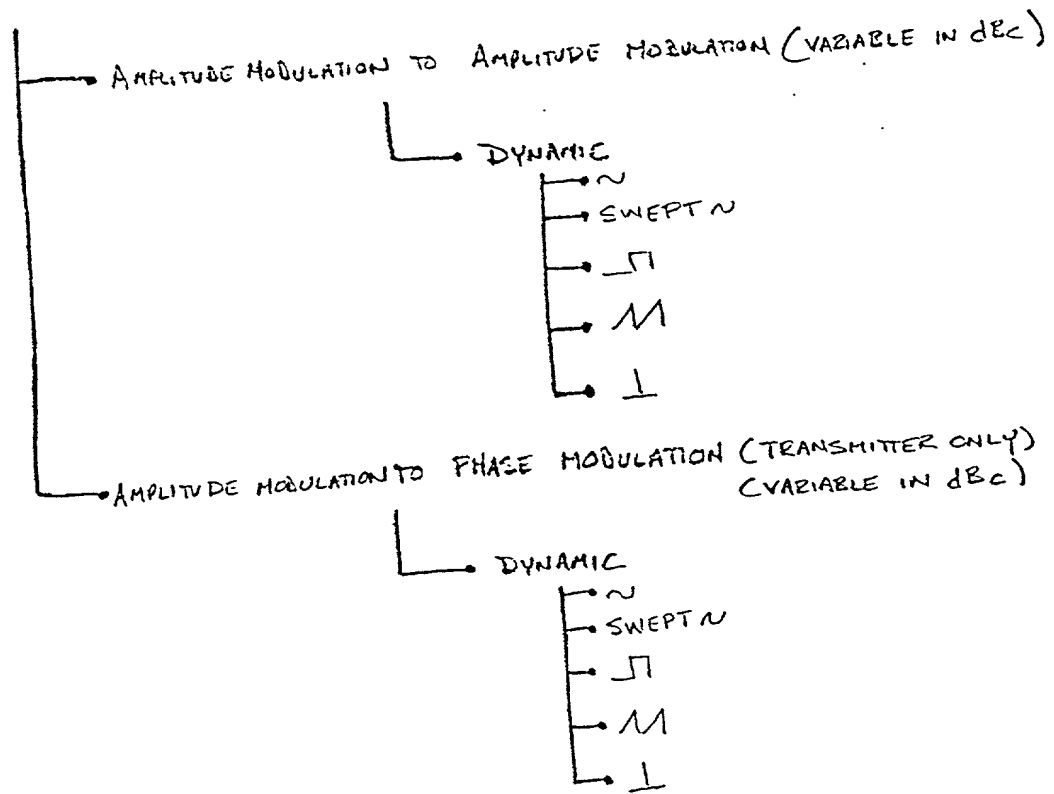


FIG. 6

## CONTROL SIGNAL IMPAIRMENTS MENU

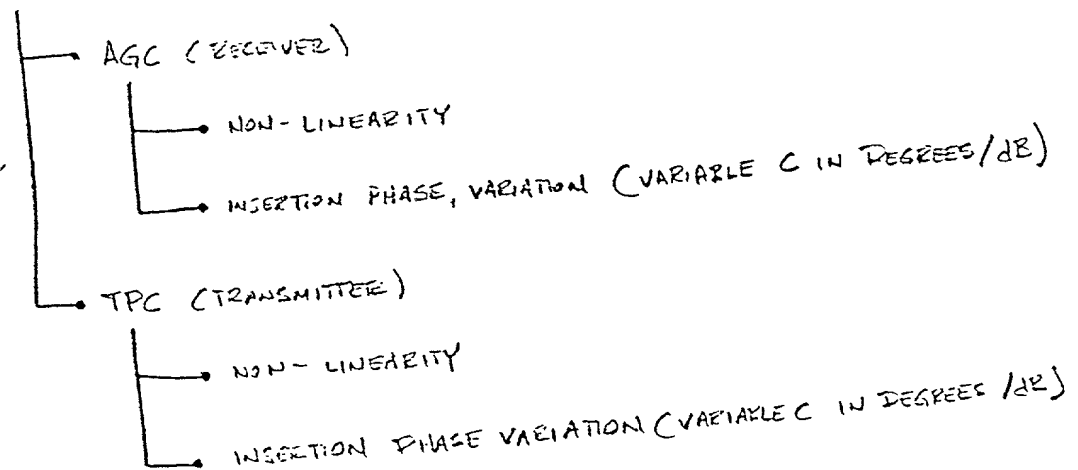
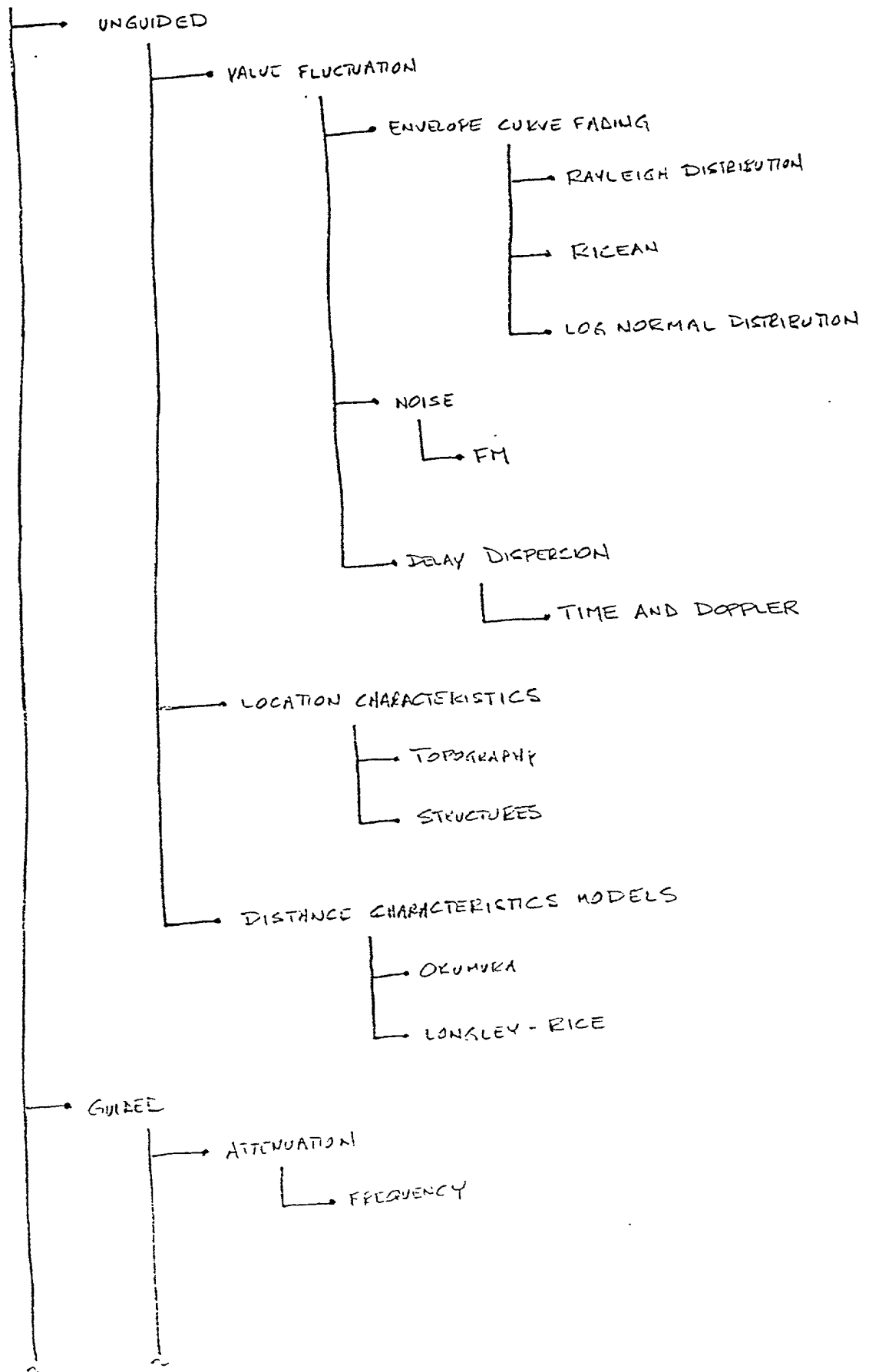


FIG. 7

# PROPAGATION IMPAIRMENTS MENU



TO FIG. 6b

FIG 8d

FROM FIG. 6a

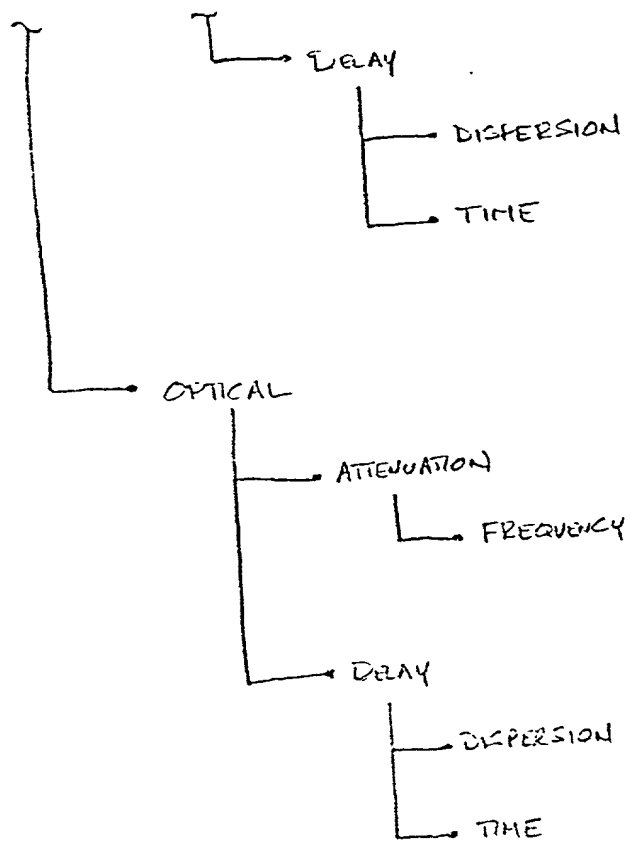


FIG. 8b

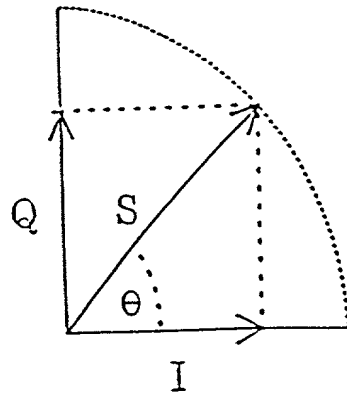


FIG. 9

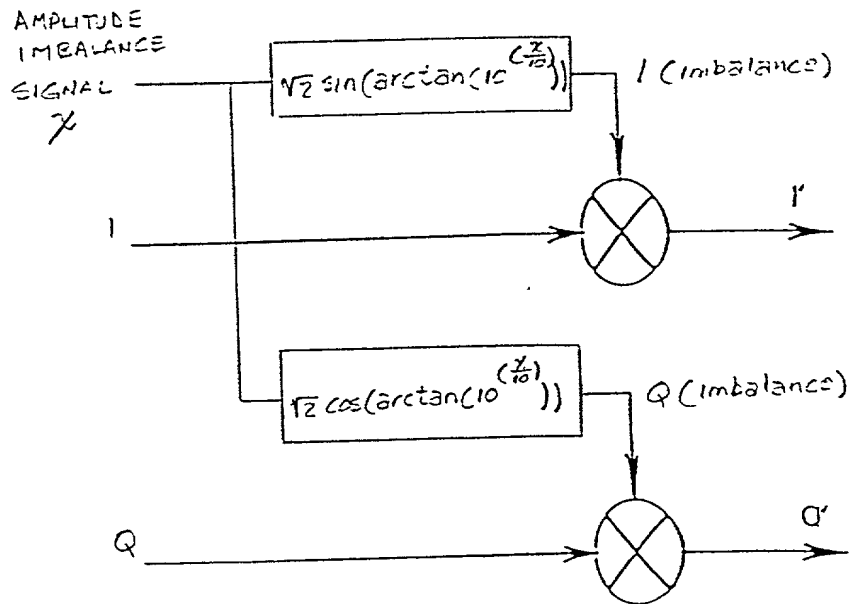


FIG. 10

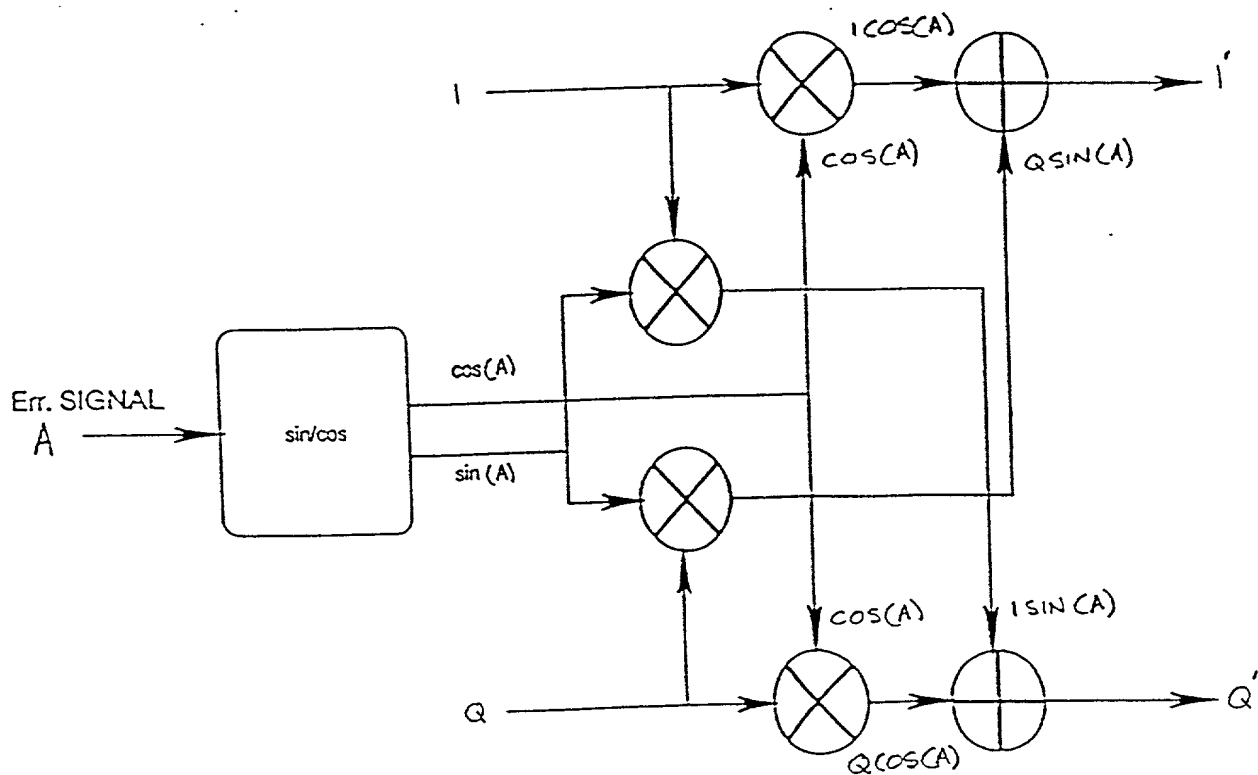


FIG. 11a

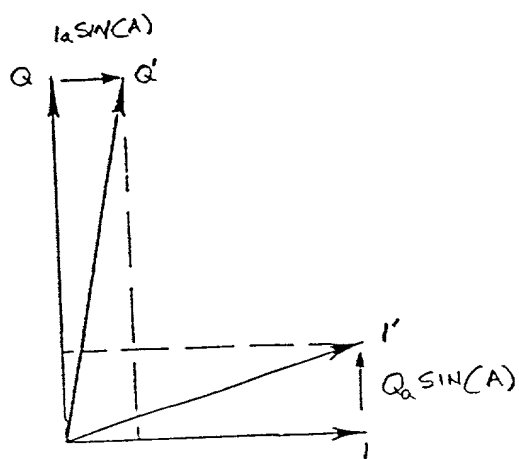


FIG. 11b

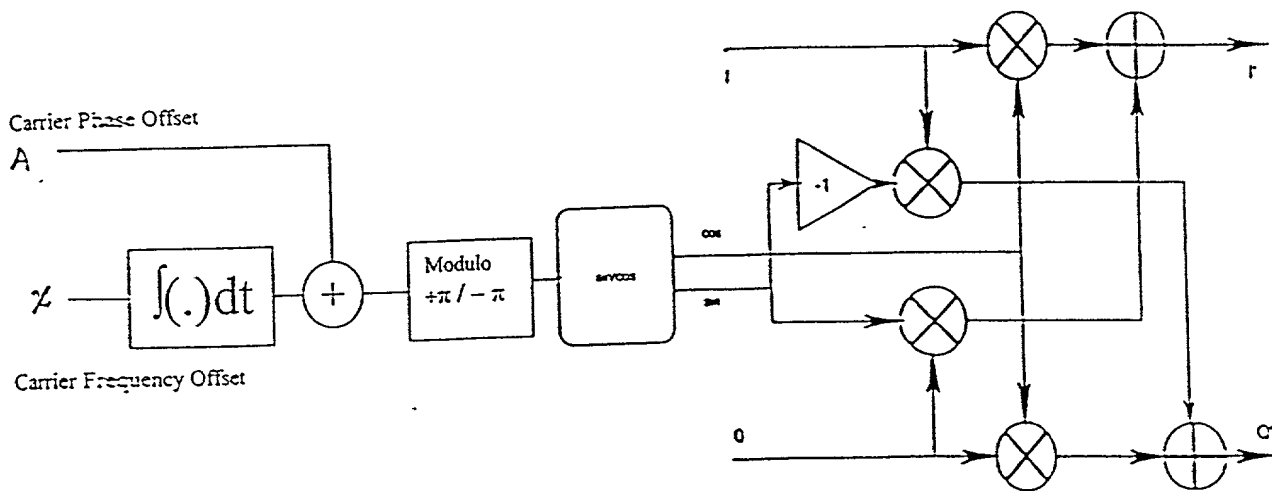


FIG. 12

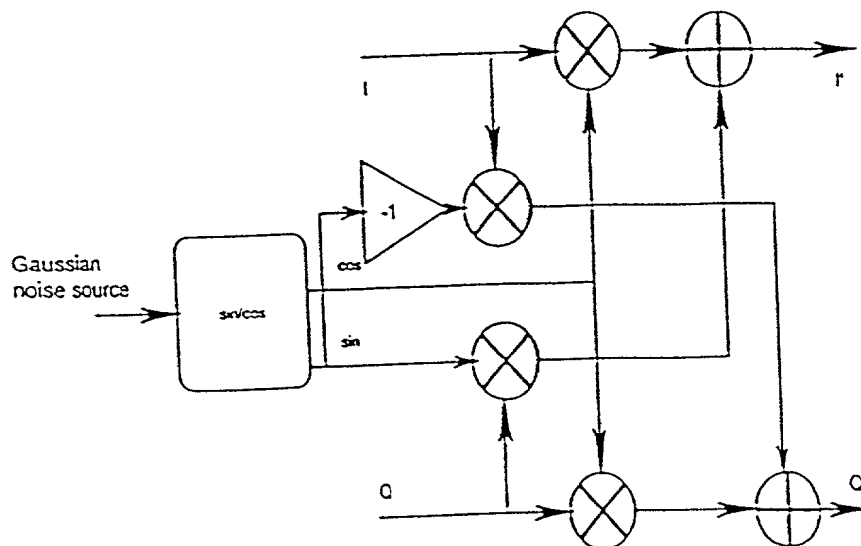


FIG. 13



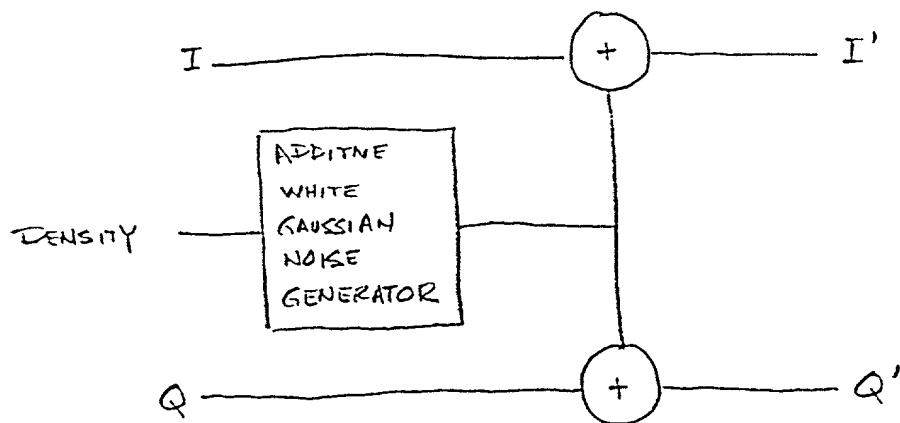


FIG. 14

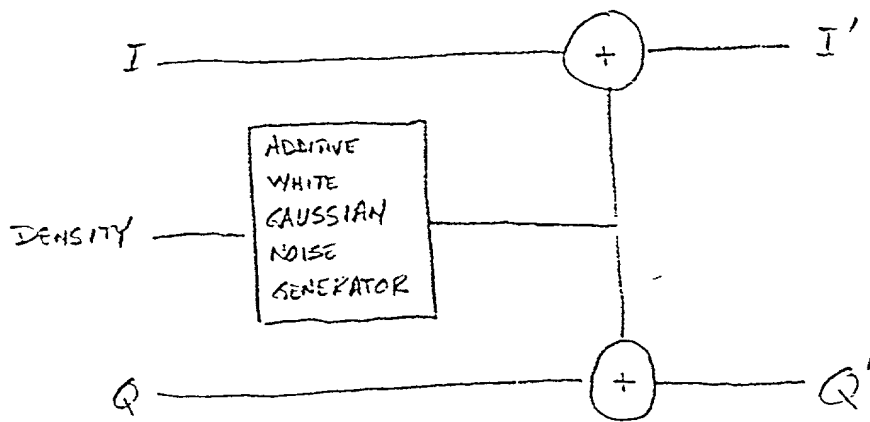


FIG. 15

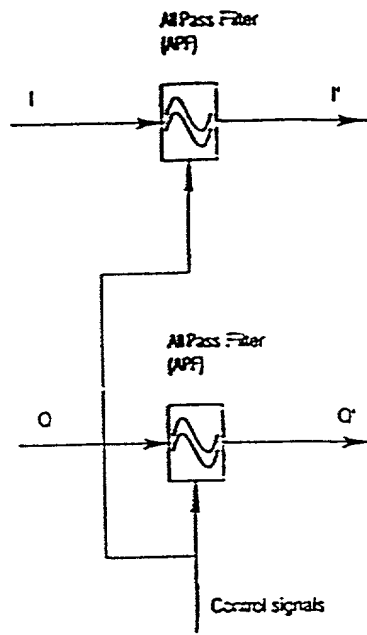


FIG. 16

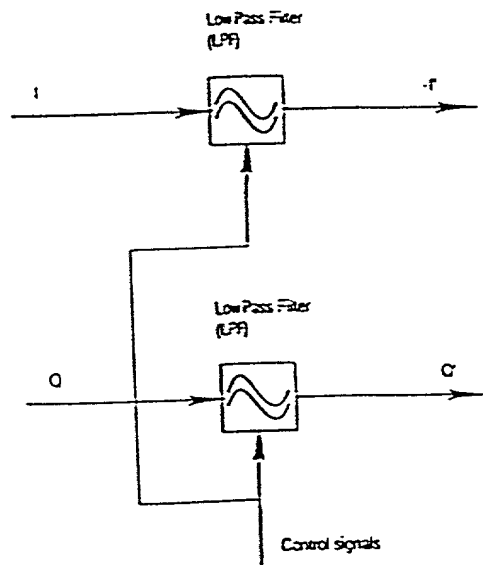


FIG. 17

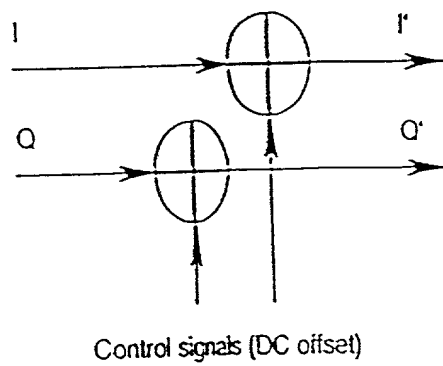


FIG. 18

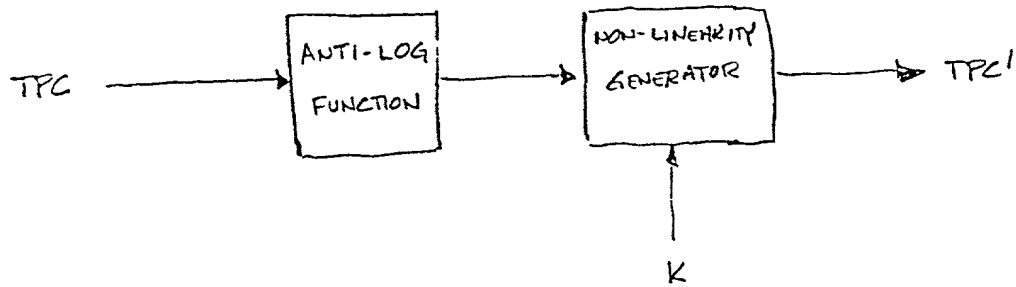


FIG. 19

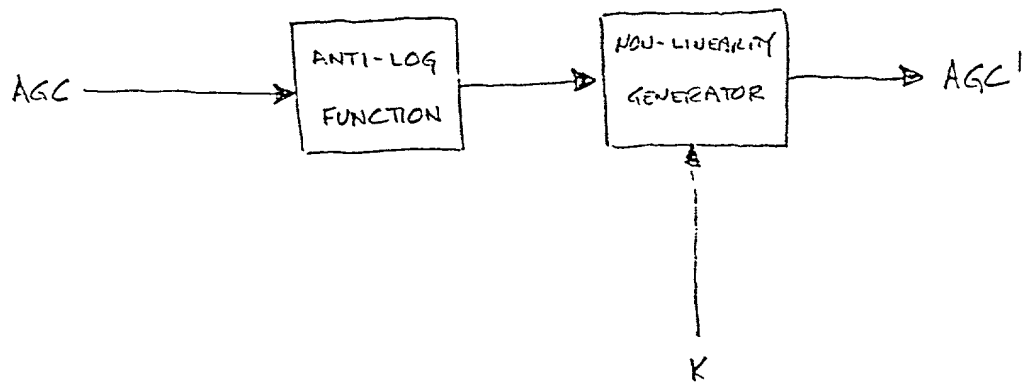


FIG. 20

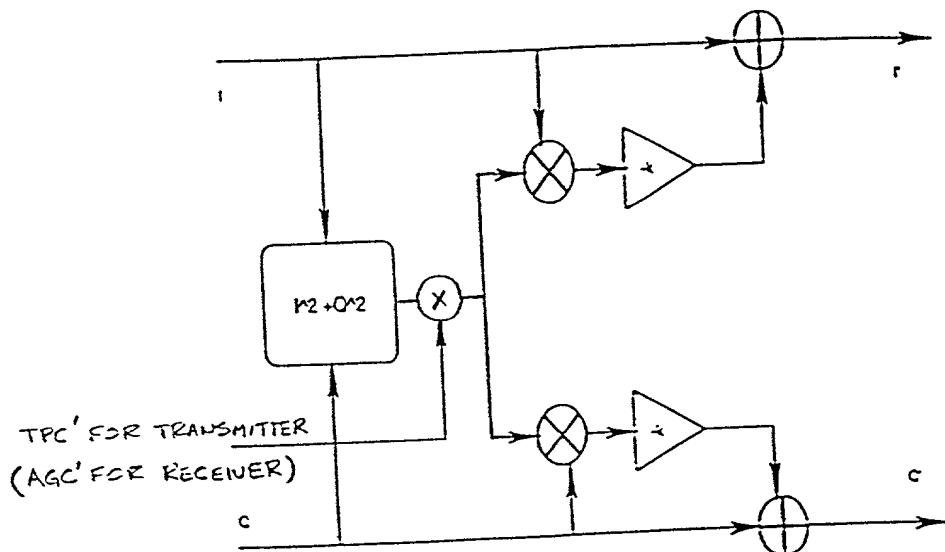


FIG. 21d

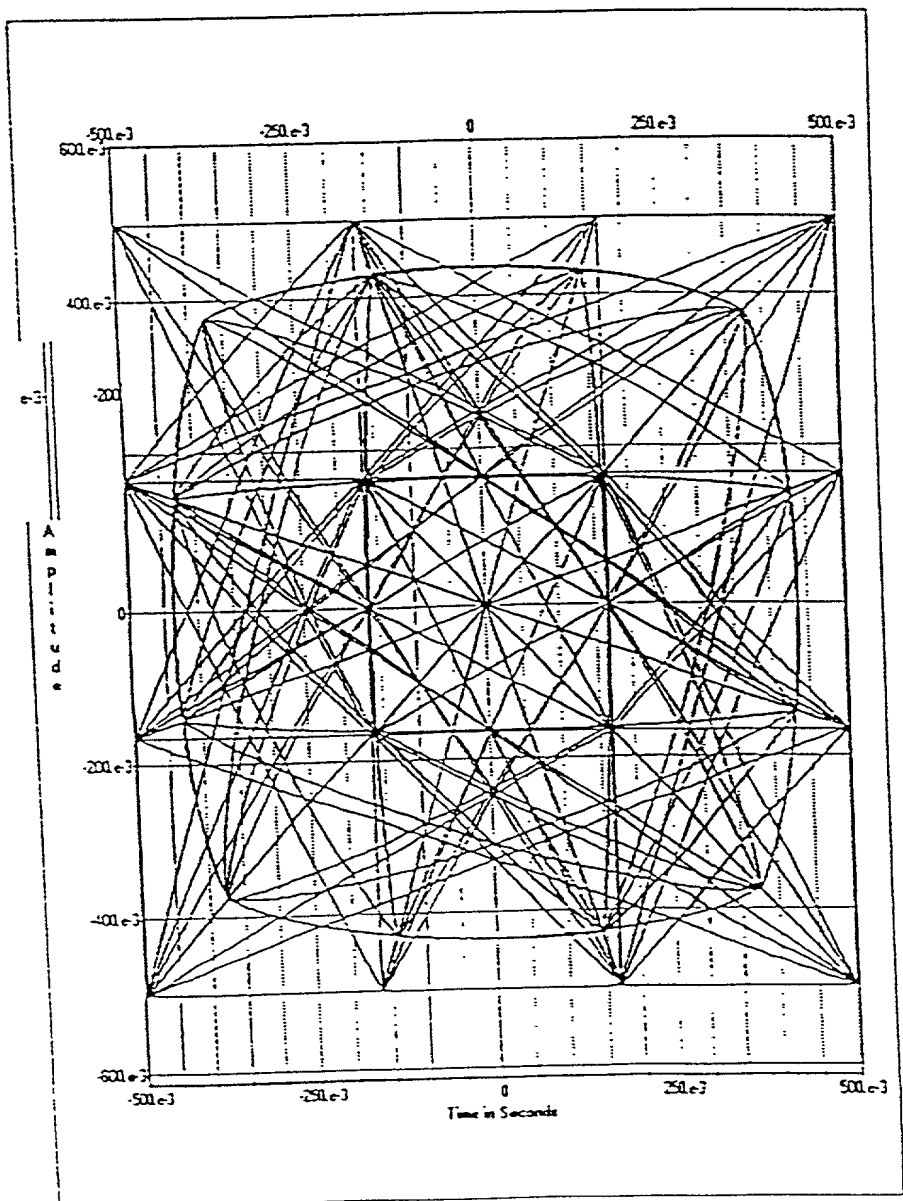


FIG 21b

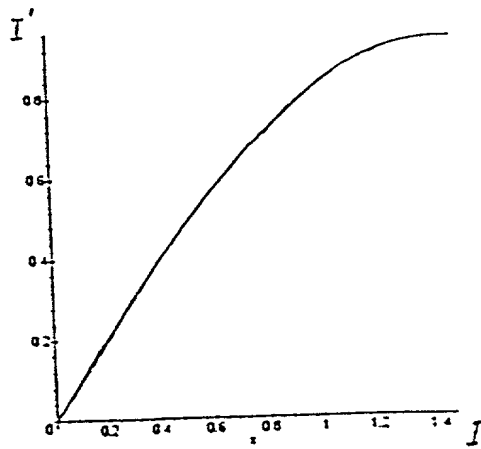


FIG. 21c

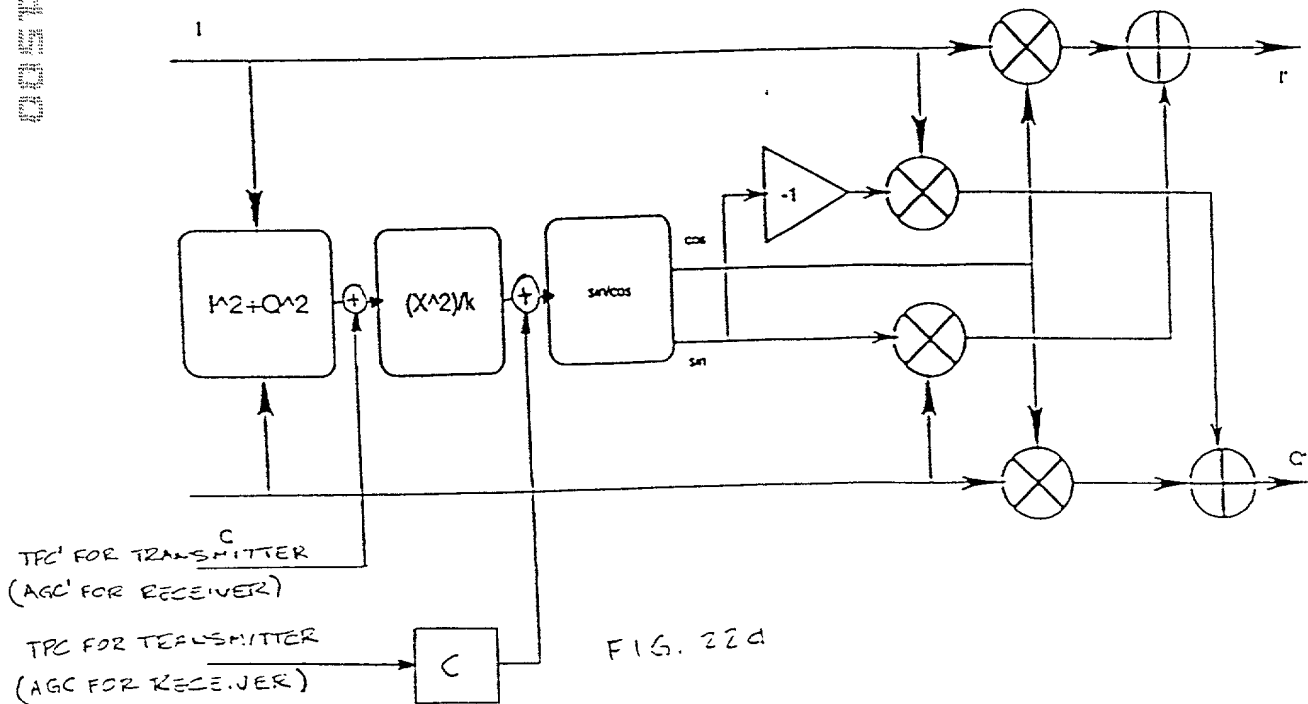


FIG. 22a

The figure is a 3D plot with a grid. The vertical axis is labeled "Amplitude" and has major ticks at  $-600 \times 10^{-3}$ ,  $-400 \times 10^{-3}$ ,  $-200 \times 10^{-3}$ ,  $0$ ,  $200 \times 10^{-3}$ ,  $400 \times 10^{-3}$ , and  $600 \times 10^{-3}$ . The horizontal axes are labeled "Time in Seconds" and have major ticks at  $-600 \times 10^{-3}$ ,  $-200 \times 10^{-3}$ ,  $-100 \times 10^{-3}$ ,  $150 \times 10^{-3}$ , and  $400 \times 10^{-3}$ . The plot shows a dense, complex network of intersecting lines, forming a multi-lobed structure that resembles a 3D wireframe model. The lines are drawn in a perspective view, creating a sense of depth. The overall shape is roughly rectangular with rounded corners and a complex internal structure of intersecting lines.

FIG 22B

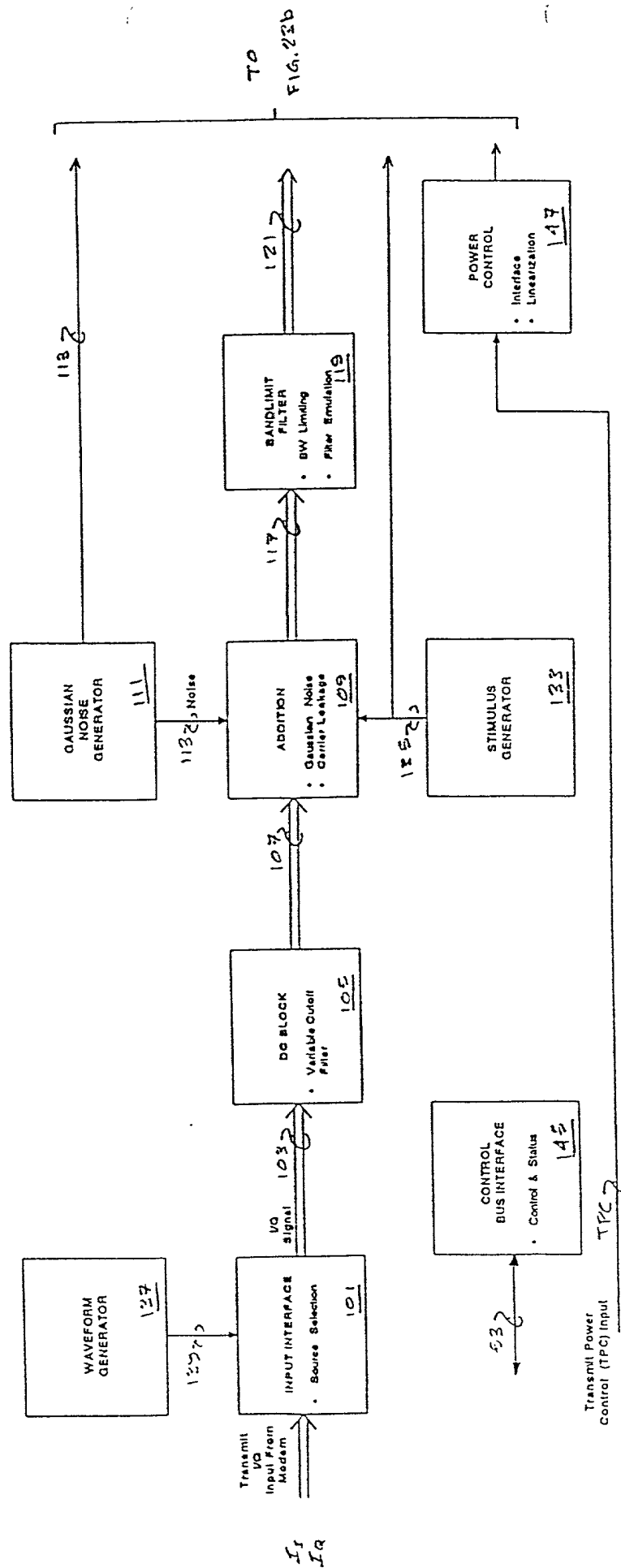


Fig. 23d

251

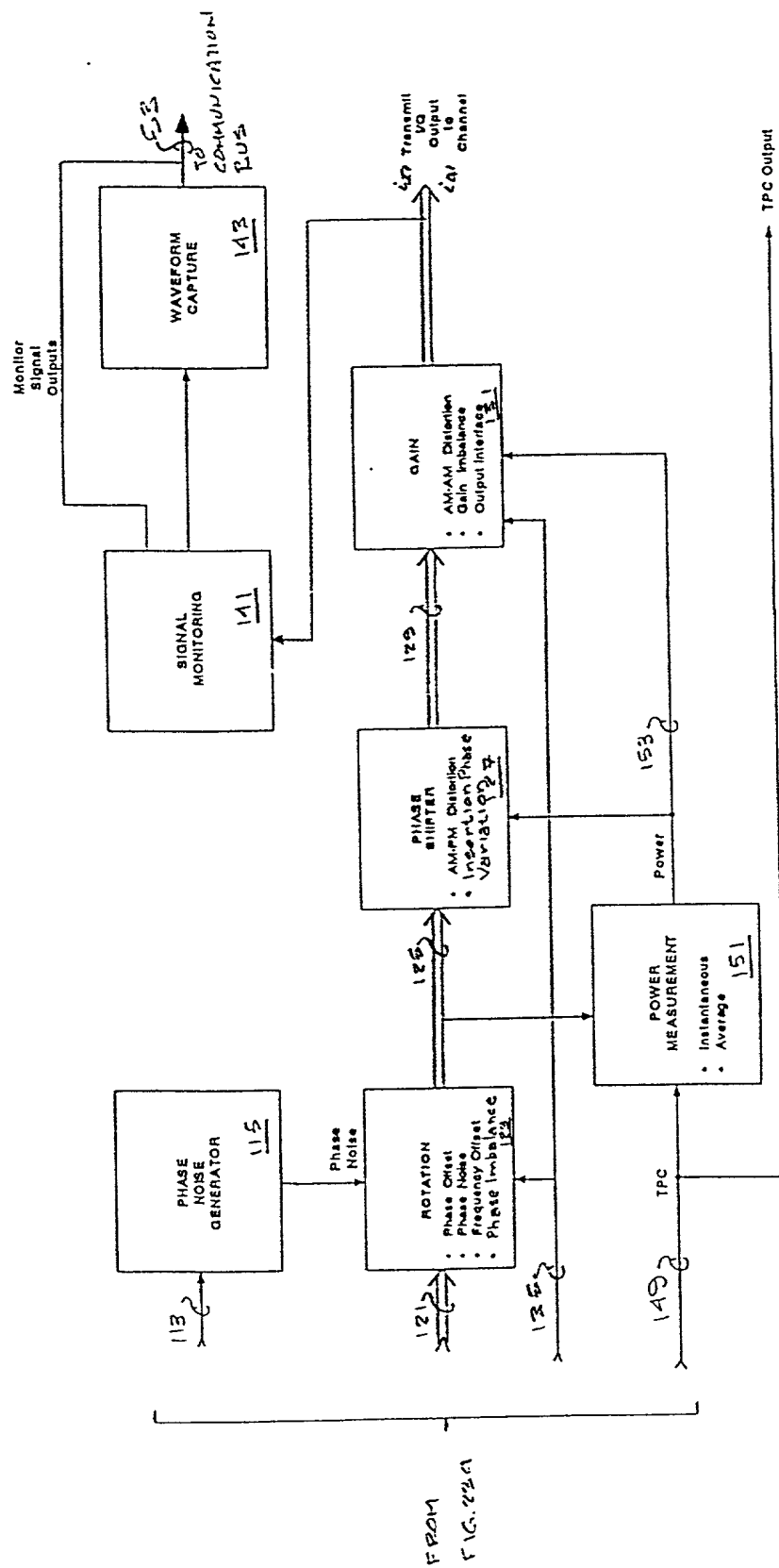


FIG. 23B



FIG. 24a

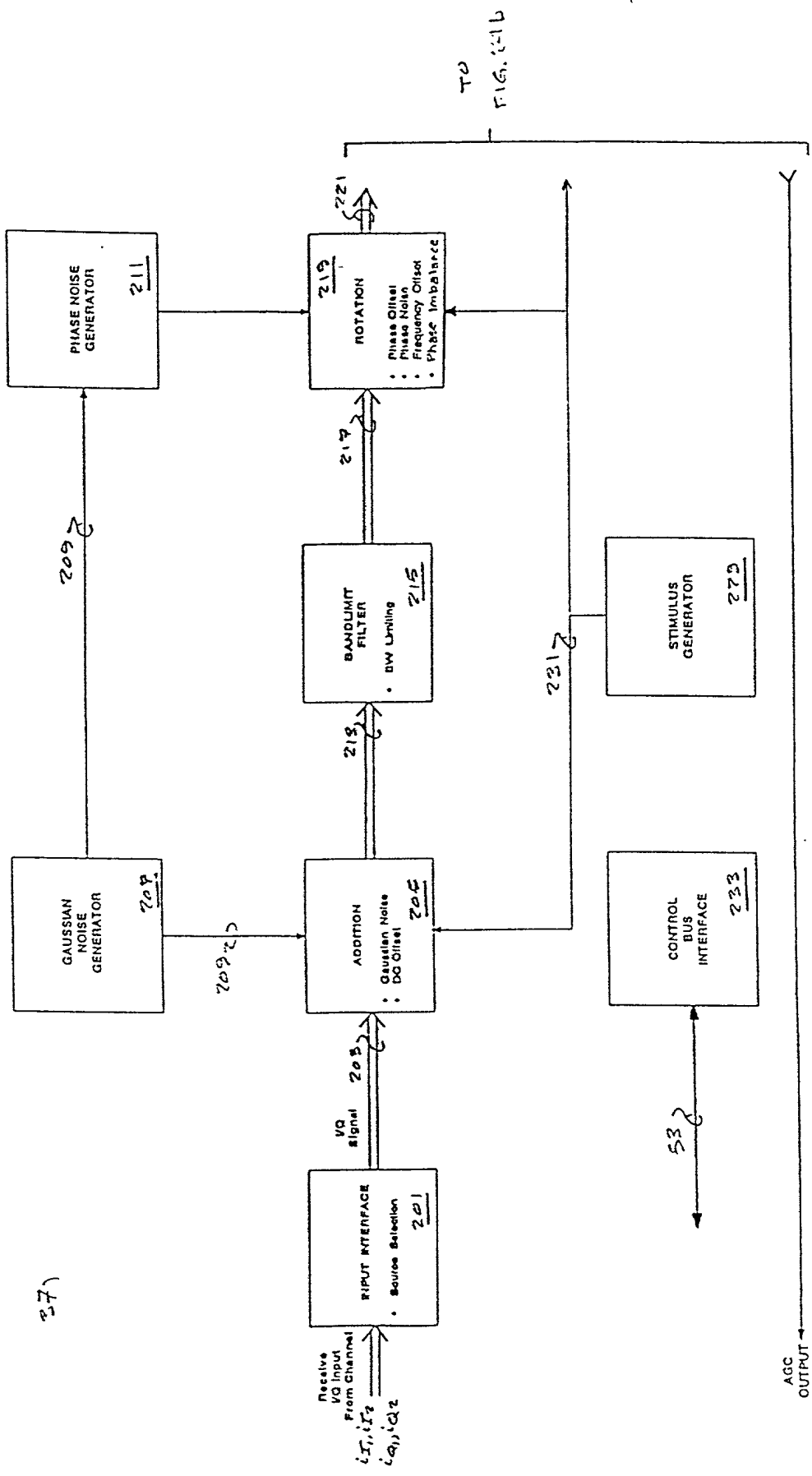


FIG. 24a

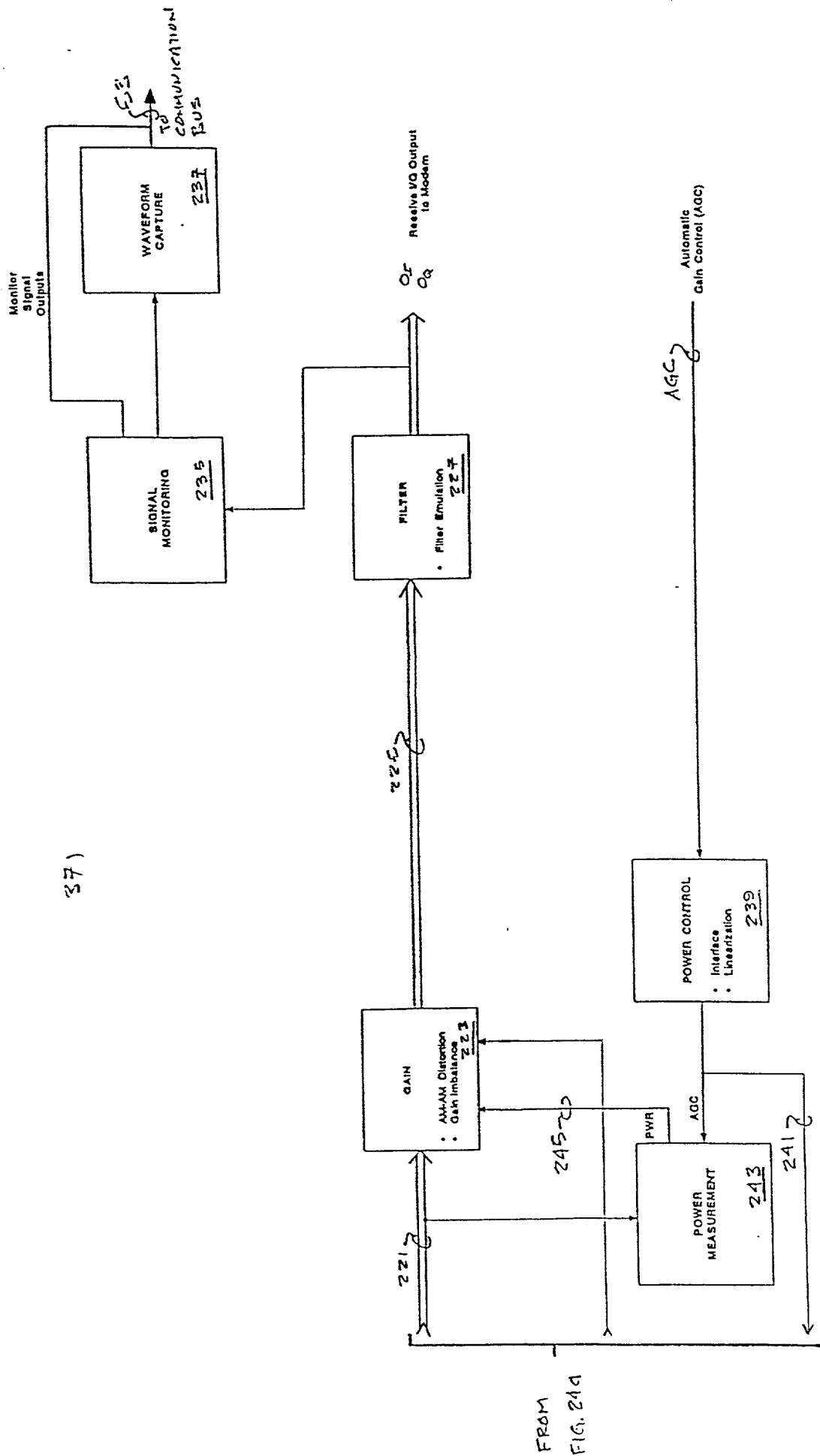


FIG. 21b

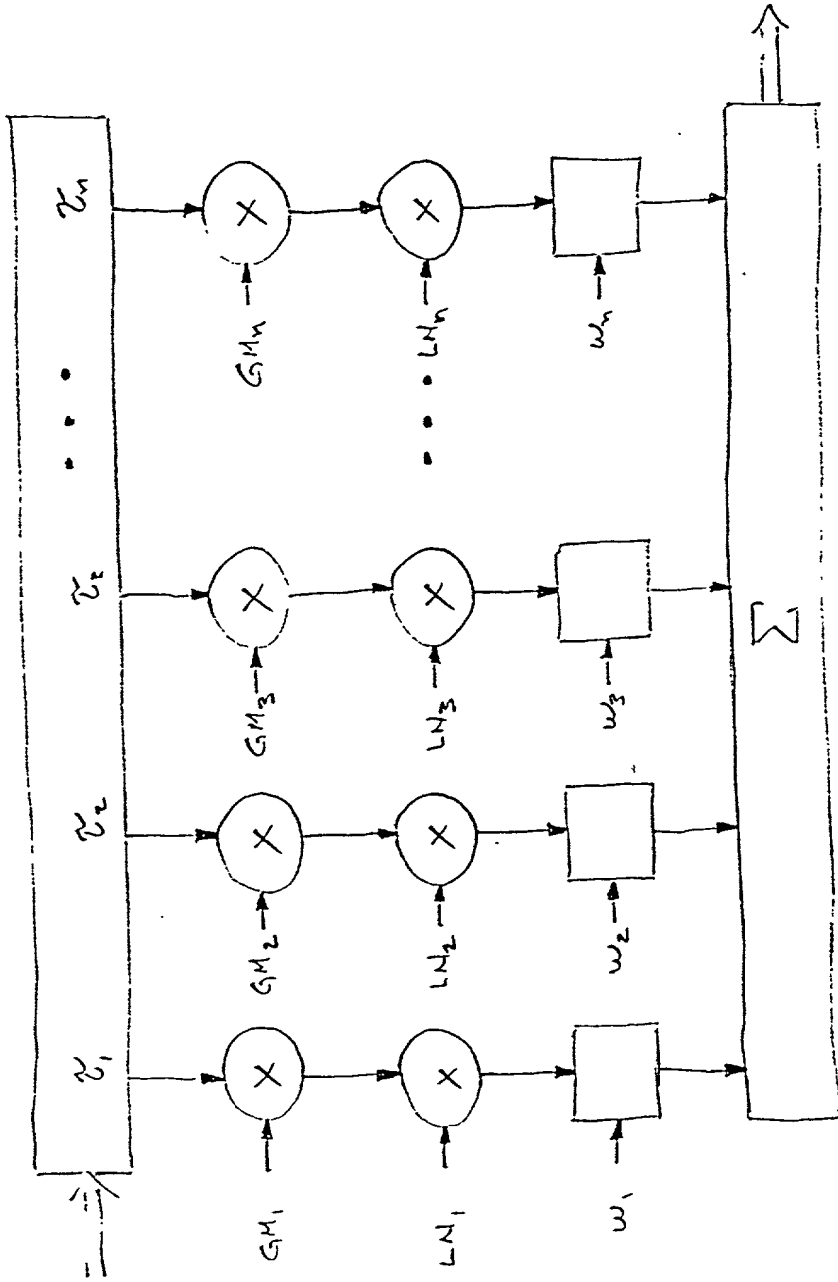


FIG. 25

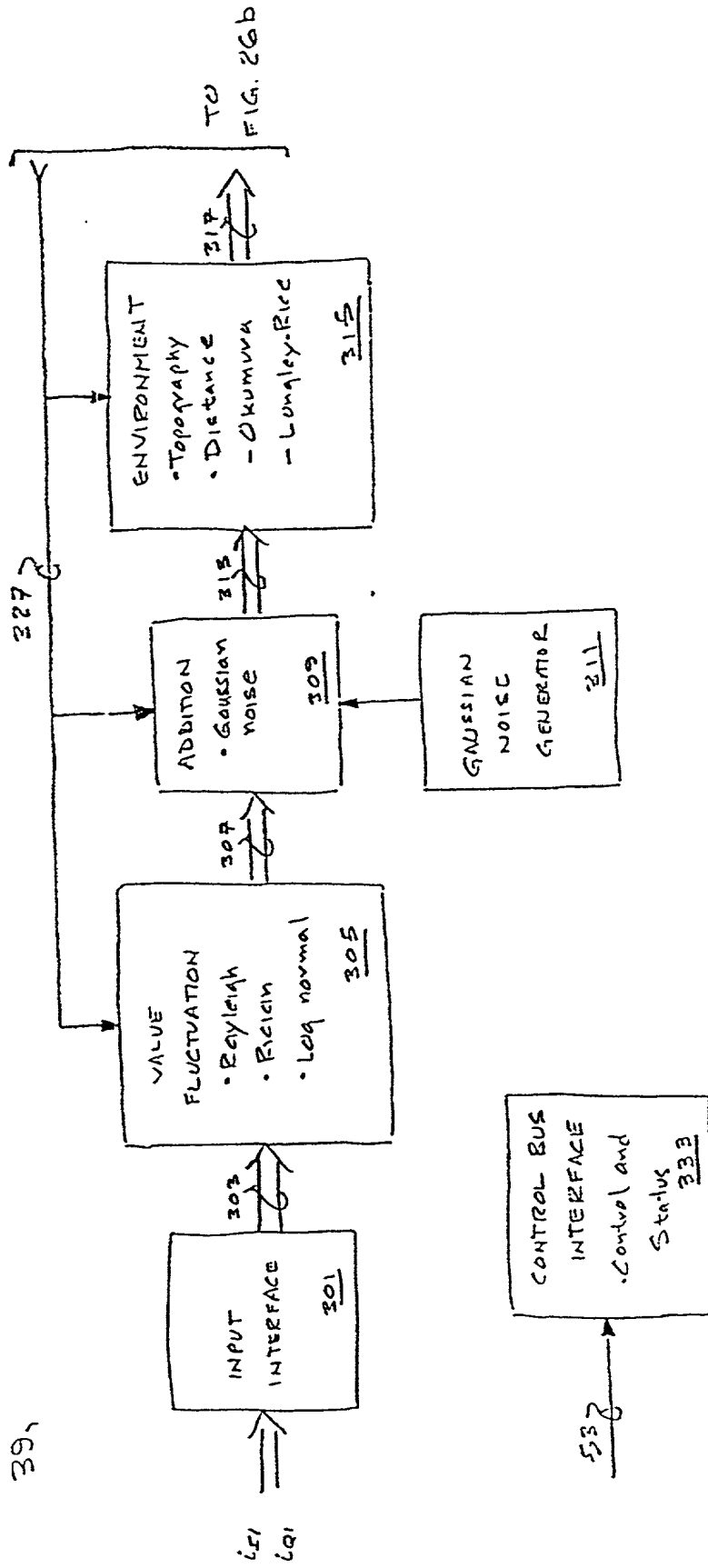


FIG. 26a

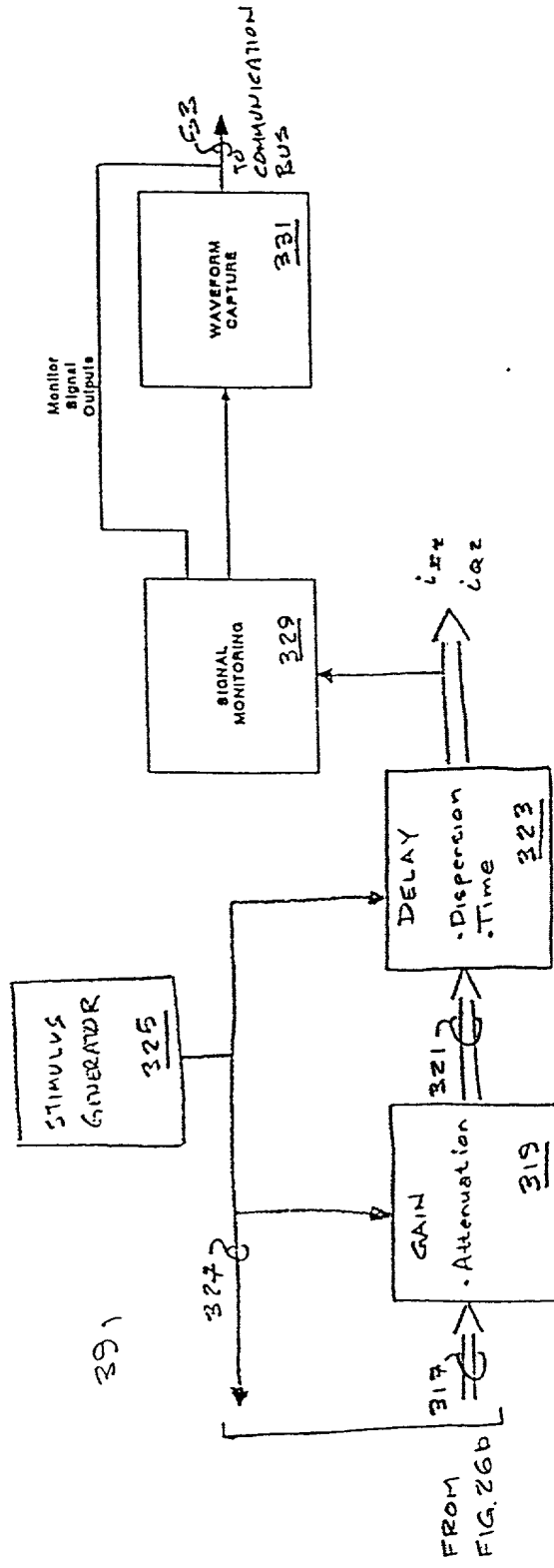


FIG. 26b

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<b>DECLARATION FOR UTILITY OR DESIGN PATENT APPLICATION (37 CFR 1.63)</b>  <input checked="" type="checkbox"/> Declaration Submitted with Initial Filing      OR <input type="checkbox"/> Declaration Submitted after Initial Filing (surcharge (37 CFR 1.16 (e)) required)	<b>Attorney Docket Number</b>	1-2-148.1US
	<b>First Named Inventor</b>	Nolan et al.
	<b>COMPLETE IF KNOWN</b>	
	<b>Application Number</b>	
	<b>Filing Date</b>	
	<b>Group Art Unit</b>	
	<b>Examiner Name</b>	

**As a below named inventor, I hereby declare that:**

My residence, post office address, and citizenship are as stated below next to my name.

I believe I am the original, first and sole inventor (if only one name is listed below) or an original, first and joint inventor (if plural names are listed below) of the subject matter which is claimed and for which a patent is sought on the invention entitled:

**COMMUNICATION TRANSMISSION IMPAIRMENT EMULATOR**

the specification of which (Title of the Invention)

☒ is attached hereto  
OR  
☐ was filed on (MM/DD/YYYY) [ ] as United States Application Number or PCT International Application Number [ ] and was amended on (MM/DD/YYYY) [ ] (if applicable).

I hereby state that I have reviewed and understand the contents of the above identified specification, including the claims, as amended by any amendment specifically referred to above.

I acknowledge the duty to disclose information which is material to patentability as defined in 37 CFR 1.56.

I hereby claim foreign priority benefits under 35 U.S.C. 119(a)-(d) or 365(b) of any foreign application(s) for patent or inventor's certificate, or 365(a) of any PCT international application which designated at least one country other than the United States of America, listed below and have also identified below, by checking the box, any foreign application for patent or inventor's certificate, or of any PCT international application having a filing date before that of the application on which priority is claimed.

Prior Foreign Application Number(s)	Country	Foreign Filing Date (MM/DD/YYYY)	Priority Not Claimed	Certified Copy Attached?	
				YES	NO
			<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
			<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
			<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
			<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>

☐ Additional foreign application numbers are listed on a supplemental priority data sheet PTO/SB/02B attached hereto:

I hereby claim the benefit under 35 U.S.C. 119(e) of any United States provisional application(s) listed below.

Application Number(s)	Filing Date (MM/DD/YYYY)	<input type="checkbox"/> Additional provisional application numbers are listed on a supplemental priority data sheet PTO/SB/02B attached hereto.
60/219,894	07/21/00	

[Page 1 of 2]

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## DECLARATION — Utility or Design Patent Application

I hereby claim the benefit under 35 U.S.C. 120 of any United States application(s), or 365(c) of any PCT international application designating the United States of America, listed below and, insofar as the subject matter of each of the claims of this application is not disclosed in the prior United States or PCT international application in the manner provided by the first paragraph of 35 U.S.C. 112, I acknowledge the duty to disclose information which is material to patentability as defined in 37 CFR 1.56 which became available between the filing date of the prior application and the national or PCT international filing date of this application.

U.S. Parent Application or PCT Parent Number	Parent Filing Date (MM/DD/YYYY)	Parent Patent Number (if applicable)

☐ Additional U.S. or PCT international application numbers are listed on a supplemental priority data sheet PTO/SB/02B attached hereto.

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☐ Registered practitioner(s) name/registration number listed below

Name	Registration Number	Name	Registration Number
Namely, the Attorneys of Volpe and Koenig, P.C. and InterDigital Communications Corporation			

☐ Additional registered practitioner(s) named on supplemental Registered Practitioner Information sheet PTO/SB/02C attached hereto.

Direct all correspondence to: ☒ Customer Number 24374 OR ☐ Correspondence address below

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Address					
Address					
City		State		ZIP	
Country		Telephone		Fax	

I hereby declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under 18 U.S.C. 1001 and that such willful false statements may jeopardize the validity of the application or any patent issued thereon.

Name of Sole or First Inventor:		<input type="checkbox"/> A petition has been filed for this unsigned inventor			
Given Name (first and middle (if any))		Family Name or Surname			
James		Nolan			
Inventor's Signature				Date	
Residence: City	Huntington	State	NY	Country	USA
				Citizenship	US
Post Office Address	2 Cardinal Court				
Post Office Address					
City	Huntington	State	NY	ZIP	11743
				Country	USA

☒ Additional inventors are being named on the 2 supplemental Additional Inventor(s) sheet(s) PTO/SB/02A attached hereto

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Supplemental Sheet**Page 1 of 2

<b>Name of Additional Joint Inventor, if any:</b>				<input type="checkbox"/> A petition has been filed for this unsigned inventor			
Given Name (first and middle [if any])				Family Name or Surname			
Leonid				Kazakevich			
Inventor's Signature						Date	
Residence: City	Plainview	State	NY	Country	USA	Citizenship	US
Post Office Address	95 Roundtree Drive						
Post Office Address							
City	Plainview	State	NY	ZIP	11803	Country	USA
<b>Name of Additional Joint Inventor, if any:</b>				<input type="checkbox"/> A petition has been filed for this unsigned inventor			
Given Name (first and middle [if any])				Family Name or Surname			
Fryderyk				Tyra			
Inventor's Signature						Date	
Residence: City	Huntington Station	State	NY	Country	USA	Citizenship	US
Post Office Address	21 Bradbury Avenue						
Post Office Address							
City	Huntington Station	State	NY	ZIP	11746	Country	USA
<b>Name of Additional Joint Inventor, if any:</b>				<input type="checkbox"/> A petition has been filed for this unsigned inventor			
Given Name (first and middle [if any])				Family Name or Surname			
Robert				Regis			
Inventor's Signature						Date	
Residence: City	Huntington	State	NY	Country	USA	Citizenship	US
Post Office Address	51-A Dunlop Road						
Post Office Address							
City	Huntington	State	NY	ZIP	11743	Country	USA

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<b>Name of Additional Joint Inventor, if any:</b>				<input type="checkbox"/> A petition has been filed for this unsigned inventor			
Given Name (first and middle [if any])				Family Name or Surname			
Fred				Schreider			
Inventor's Signature						Date	
Residence: City	Commack	State	NY	Country	USA	Citizenship	US
Post Office Address	49 Pawnee Drive						
Post Office Address							
City	Commack	State	NY	ZIP	11725	Country	USA
<b>Name of Additional Joint Inventor, if any:</b>				<input type="checkbox"/> A petition has been filed for this unsigned inventor			
Given Name (first and middle [if any])				Family Name or Surname			
Inventor's Signature						Date	
Residence: City		State		Country		Citizenship	
Post Office Address							
Post Office Address							
City		State		ZIP		Country	
<b>Name of Additional Joint Inventor, if any:</b>				<input type="checkbox"/> A petition has been filed for this unsigned inventor			
Given Name (first and middle [if any])				Family Name or Surname			
Inventor's Signature						Date	
Residence: City		State		Country		Citizenship	
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